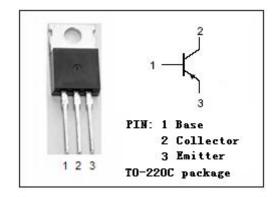


# isc Silicon PNP Power Transistor

#### **DESCRIPTION**

- · Collector-Emitter Breakdown Voltage-
  - : V<sub>(BR)CEO</sub>= -20V(Min)
- · High Speed Switching
- · Low Collector Saturation Voltage
  - :  $V_{CE(sat)} = -0.6V(Max)@I_C = -10A$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

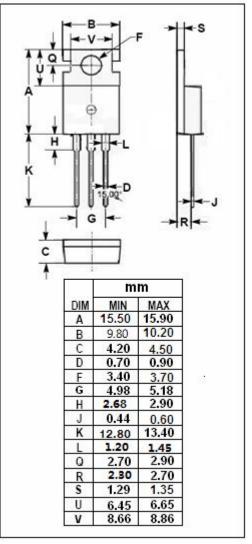


### **APPLICATIONS**

• Designed for low voltage switching applications.

### ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
$V_{\text{CBO}}$	Collector-Base Voltage	-40	V	
$V_{\text{CEO}}$	Collector-Emitter Voltage	-20	V	
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V	
Ic	Collector Current-Continuous	-10	A	
Ісм	Collector Current-Peak	-20	А	
P <sub>C</sub>	Collector Power Dissipation @ T <sub>C</sub> =25℃	40	W	
TJ	T <sub>J</sub> Junction Temperature		°C	
T <sub>stg</sub>	T <sub>stg</sub> Storage Temperature Range		°C	





# isc Silicon PNP Power Transistor

2SB871

### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)</sub> CEO	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10mA; I <sub>B</sub> = 0	-20			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -10A; I <sub>B</sub> = -0.33A			-0.6	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = -10A; I <sub>B</sub> = -0.33A			-1.5	V
Ісво	Collector Cutoff Current	V <sub>CB</sub> = -40V; I <sub>E</sub> = 0			-50	μА
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-50	μА
h <sub>FE-1</sub>	DC Current Gain	Ic= -0.1A; V <sub>CE</sub> = -2V	45			
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -3A; V <sub>CE</sub> = -2V	60		260	

### ♦ h<sub>FE-2</sub> Classifications

R	Q	Р
60-120	90-180	130-260

#### NOTICE:

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