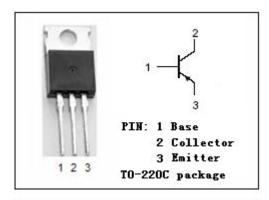


isc Silicon PNP Power Transistor

DESCRIPTION

- Collector Current: I_C= -4A
- · Low Collector Saturation Voltage
 - : $V_{CE(sat)}$ = -2.0V(Max)@I_C= -2A
- · High Collector Power Dissipation
- · Complement to Type 2SD1135
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

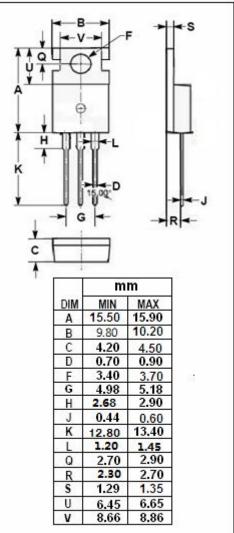


APPLICATIONS

• Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-100	V
V _{CEO}	Collector-Emitter Voltage		V
V _{EBO}	Emitter-Base Voltage -5		V
Ic	Collector Current-Continuous	-4	А
Ісм	Collector Current-Peak	-8	А
Pc	Total Power Dissipation @ T _C =25℃	40	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature Range	-45~150	$^{\circ}$





isc Silicon PNP Power Transistor

2SB859

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I_{C} = -30mA ; R_{BE} = ∞	-80			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -10 μ A ; I _C = 0	-5			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -2A; I _B = -0.2A			-2.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -1A ; V _{CE} = -5V			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -80V; I _E = 0			-100	μА
h _{FE-1}	DC Current Gain	I _C = -1A; V _{CE} = -5V	60		200	
h _{FE-2}	DC Current Gain	I _C = -0.1A; V _{CE} = -5V	35			
Сов	Collector Output Capacitance	I _E = 0; V _{CB} = -10V; f= 1MHz		75		pF
f _T	Current-Gain—Bandwidth Product	I _C = -0.5A; V _{CE} = -5V		20		MHz

♦ h_{FE-1} Classifications

В	С
60-120	100-200

NOTICE:

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