

isc Silicon PNP Power Transistors

2SB653

DESCRIPTION

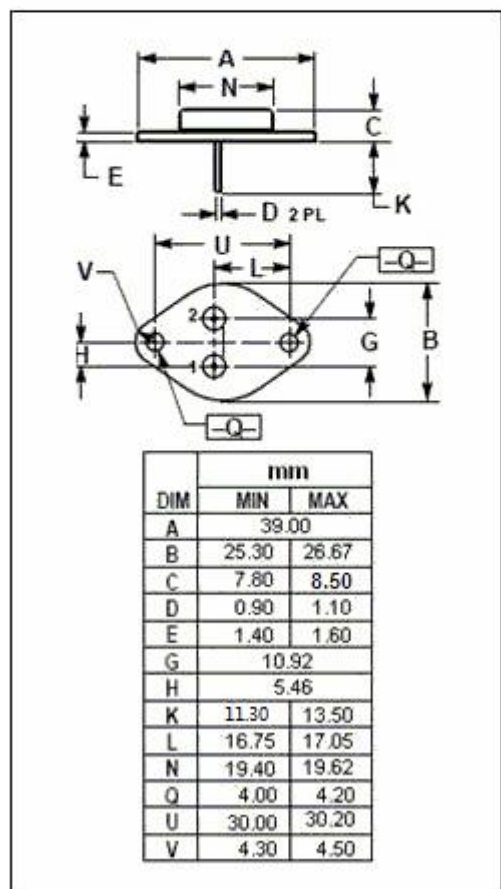
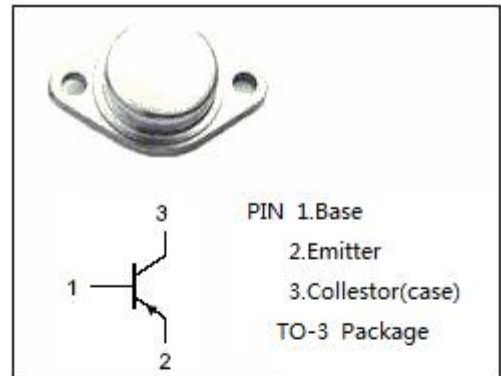
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -100V(\text{Min})$
- High Power Dissipation-
: $P_C = 60W(\text{Max})@T_C=25^\circ\text{C}$
- Complement to Type 2SD673
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-120	V
V_{CEO}	Collector-Emitter Voltage	-100	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-7	A
I_{CM}	Collector Current-Peak	-12	A
I_B	Base Current-Continuous	-2	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	60	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$



isc Silicon PNP Power Transistors**2SB653****ELECTRICAL CHARACTERISTICS**

Tj=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C = -50mA$; $R_{BE} = \infty$	-100			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = -5mA$; $I_C = 0$	-5			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -5A$; $I_B = -0.5A$			-3.0	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C = -1A$; $V_{CE} = -5V$			-1.5	V
I_{CBO}	Collector Cutoff Current	$V_{CB} = -100V$; $I_E = 0$			-0.1	mA
h_{FE-1}	DC Current Gain	$I_C = -1A$; $V_{CE} = -5V$	60		200	
h_{FE-2}	DC Current Gain	$I_C = -5A$; $V_{CE} = -5V$	20			

◆ **h_{FE} Classifications**

B	C
60-120	100-200

NOTICE:

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