

## **isc Silicon PNP Power Transistor**

# 2SB548

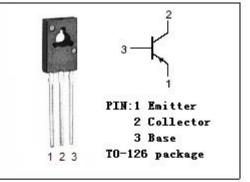
#### DESCRIPTION

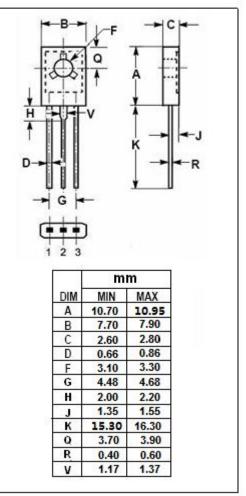
- Collector-Emitter Breakdown Voltage-: V<sub>(BR)CEO</sub>= -80V(Min)
- With TO-126 package
- Complement to Type 2SD414
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATIONS

• Designed for low frequency power amplifiers applications.

#### ABSOLUTE MAXIMUM RATINGS(Ta=25℃) SYMBOL PARAMETER VALUE UNIT Collector-Base Voltage -100 V V<sub>сво</sub> -80 Collector-Emitter Voltage V VCEO VEBO Emitter-Base Voltage -5 V Collector Current-Continuous lc -0.8 А Collector Current-Peak -1.5 Ісм А **Collector Power Dissipation** 1 @Ta=25℃ Pc W **Collector Power Dissipation** 10 @Tc=25℃ Junction Temperature 150 °C ΤJ -55~150 °C Storage Temperature Tstg





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### **ELECTRICAL CHARACTERISTICS**

#### Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1mA; R <sub>BE</sub> =∞	-80			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -0.5A; I <sub>B</sub> = -50mA			-2.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -0.5A; I <sub>B</sub> = -50mA			-1.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -80V; I <sub>E</sub> = 0			-1	μA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -3V; I <sub>C</sub> = 0			-1	μA
h <sub>FE -1</sub>	DC Current Gain	Ic= -2mA; Vce= -5V	20			
h <sub>FE -2</sub>	DC Current Gain	I <sub>C</sub> = -0.2A; V <sub>CE</sub> = -5V	40		320	
Сов	Output Capacitance	I <sub>E</sub> =0; V <sub>CB</sub> = -10V; f= 1MHz		25		pF
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> =-0.1A ; V <sub>CE</sub> = -5V		70		MHz
hFF .2Clas	sifications					

S	R	Q
40-80	60-120	100-200

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