

isc Silicon PNP Power Transistor

DESCRIPTION

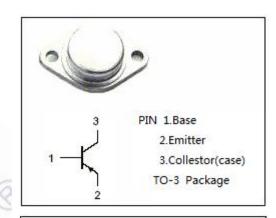
- · Collector-Emitter Sustaining Voltage-
 - : V_{CEO(SUS)}= -65V(Min)
- · Low Collector Saturation Voltage-
 - : $V_{CE(sat)}$ = -2.0V(Max.) @I_C= -10A
- · Wide area of safe operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

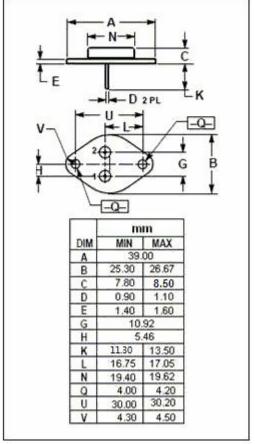
APPLICATIONS

• Designed for power amplifier applications.



SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-65	٧
Vceo	Collector-Emitter Voltage	-65	V
V _{EBO}	Emitter-Base Voltage	-8	V
Ic	Collector Current-Continuous	-15	А
Pc	Collector Power Dissipation @Tc=25℃	75	W
TJ	Junction Temperature	150	${\mathbb C}$
T _{stg}	Storage Temperature	-55~150	${\mathbb C}$







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2SB538

ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -10mA; I _B = 0	-65			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -1mA; I _C = 0	-5			V
V _{(BR)CBO}	Collector-Base breakdown voltage	Ic=-1mA; I _E = 0	-65			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -10A; I _B = -1A			-2.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -10A; I _B = -1A			-2.5	V
Ісво	Collector Cutoff Current	V _{CB} = -65V; I _E = 0			-100	μА
I _{CEO}	Collector Cutoff Current	V _{CE} = -65V; I _B = 0			-100	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -7V; I _C = 0			-10	μА
h _{FE -1}	DC Current Gain	I _C = -2A; V _{CE} = -5V	40		140	
h _{FE -2}	DC Current Gain	I _C = -10A; V _{CE} = -5V	20			
f _T	Current-Gain—Bandwidth Product	I _C =-0.5A; V _{CE} = -10V		15		MHz

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