

ISC Silicon PNP Power Transistor

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
- : V_{CEO(SUS)}= -140V(Min)
- · Low Collector Saturation Voltage-
- : V_{CE(sat)}= -1.0V(Max.) @I_C= -7A
- · Wide area of safe operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

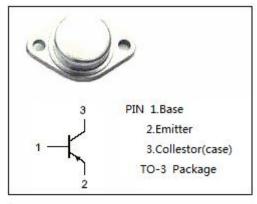


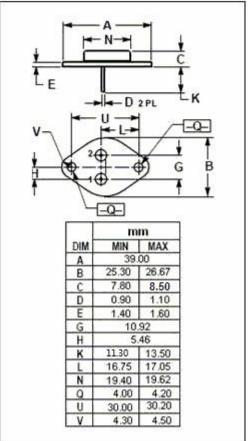
APPLICATIONS

Designed for general-purpose switching and amplifier applications



SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-200	V
Vceo	Collector-Emitter Voltage -1		V
V _{EBO}	Emitter-Base Voltage	-8	V
Ic	Collector Current-Continuous	-12	А
Pc	Collector Power Dissipation @Tc=25℃	100	W
TJ	Junction Temperature	150	$^{\circ}\mathbb{C}$
T _{stg}	Storage Temperature	-55~150	${\mathbb C}$







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2SB520

ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -10mA; I _B = 0	-140			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -1mA; I _C = 0	-8			V
V _{(BR)CBO}	Collector-Base breakdown voltage	I _C =-1mA; I _E = 0	-200			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -7A; I _B = -0.7A			-1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -7A; I _B = -0.7A			-1.5	V
Ісво	Collector Cutoff Current	V _{CB} = -200V; I _E = 0			-100	μА
I _{CEO}	Collector Cutoff Current	V _{CE} = -140V; I _B = 0			-100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -8V; I _C = 0			-10	μ A
h _{FE}	DC Current Gain	I _C = -5A; V _{CE} = -2V	50			
f⊤	Current-Gain—Bandwidth Product	I _C =-0.5A ; V _{CE} = -10V		15		MHz

Notice:

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