

isc Silicon PNP Power Transistor

2SB1372

DESCRIPTION

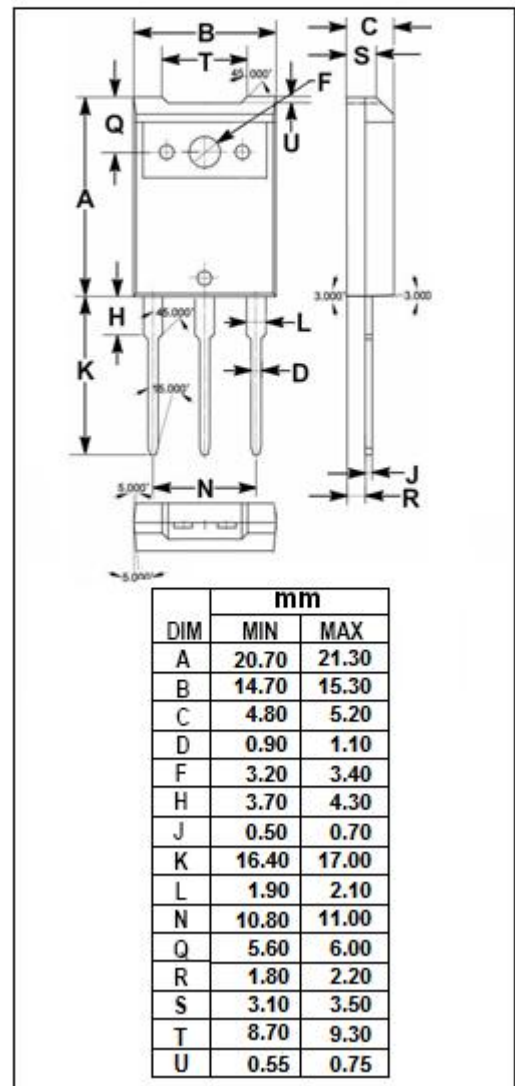
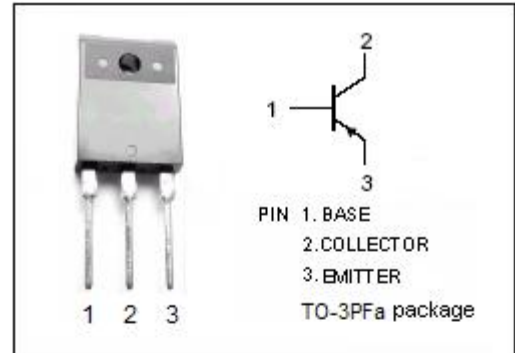
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -140V(\text{Min})$
- Good Linearity of h_{FE}
- Wide Area of Safe Operation
- Complement to Type 2SD2065
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high power amplifications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-140	V
V_{CEO}	Collector-Emitter Voltage	-140	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-7	A
I_{CP}	Collector Current-Pulse	-12	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	80	W
	Collector Power Dissipation @ $T_a=25^\circ\text{C}$	3	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon PNP Power Transistor**2SB1372****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A			-2.0	V
V _{BE(on)}	Base -Emitter On Voltage	I _C = -5A; V _{CE} = -5V			-1.8	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -140V; I _E = 0			-50	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -3V; I _C = 0			-50	μ A
h _{FE-1}	DC Current Gain	I _C = -20mA; V _{CE} = -5V	20			
h _{FE-2}	DC Current Gain	I _C = -1A; V _{CE} = -5V	60		200	
h _{FE-3}	DC Current Gain	I _C = -5A; V _{CE} = -5V	20			
f _T	Current-Gain—Bandwidth Product	I _C = -0.5A; V _{CE} = -5 V; f= 1MHz		15		MHz
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = -10V; f= 1MHz		200		pF

◆ **h_{FE-2}Classifications**

Q	S	P
60-120	80-160	100-200

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