

isc Silicon PNP Power Transistor

2SB1345

DESCRIPTION

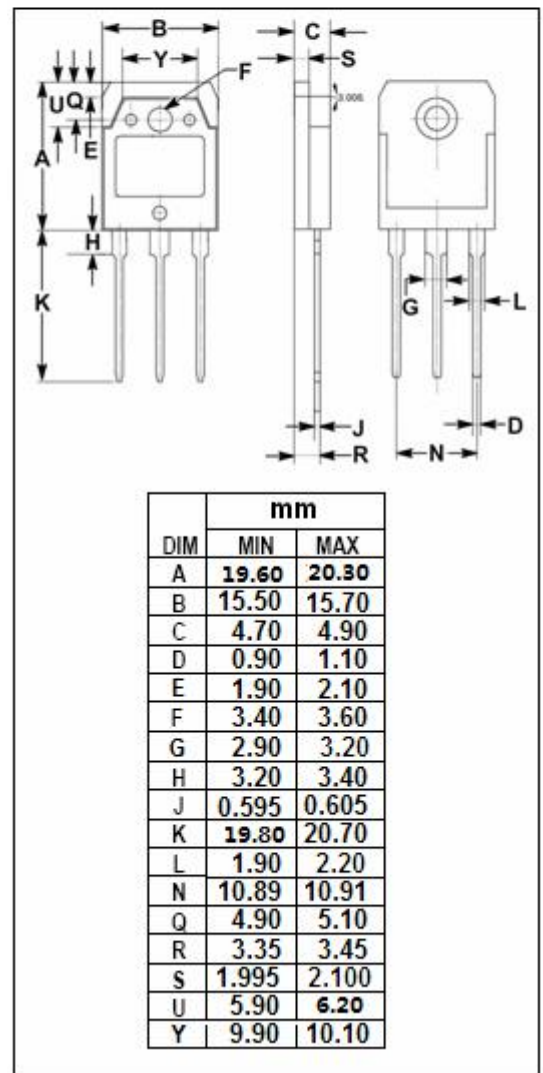
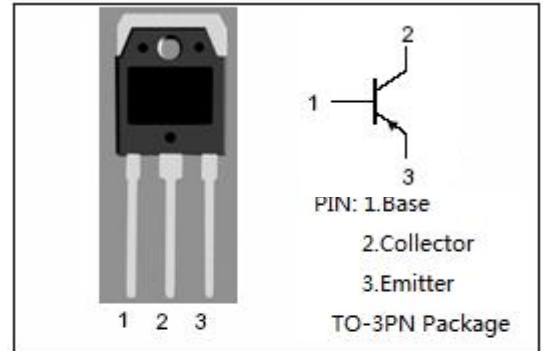
- Low Collector Saturation Voltage-
: $V_{CE(sat)} = -2.0V(\text{Min}) @ I_C = -5A$
- Good Linearity of h_{FE}
- Complement to Type 2SD2062
- With TO-3PN package
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Power driver and general purpose applications

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-100	V
V_{CEO}	Collector-Emitter Voltage	-80	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-7	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ\text{C}$	80	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon PNP Power Transistor**2SB1345****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C = -10\text{mA}$; $I_B = 0$	-80			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -5\text{A}$; $I_B = -0.5\text{A}$			-2.0	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = -5\text{A}$; $I_B = -0.5\text{A}$			-2.5	V
I_{CBO}	Collector Cutoff Current	$V_{CB} = -100\text{V}$; $I_E = 0$			-10	μA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -5\text{V}$; $I_C = 0$			-10	μA
h_{FE}	DC Current Gain	$I_C = -1\text{A}$; $V_{CE} = -5\text{V}$	60		320	
f_T	Current-Gain—Bandwidth Product	$I_C = -0.5\text{A}$; $V_{CE} = -12\text{V}$		12		MHz

◆ h_{FE} Classifications

D	E	F
60-120	100-200	160-320

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