

isc Silicon PNP Power Transistor

2SAR573D

DESCRIPTION

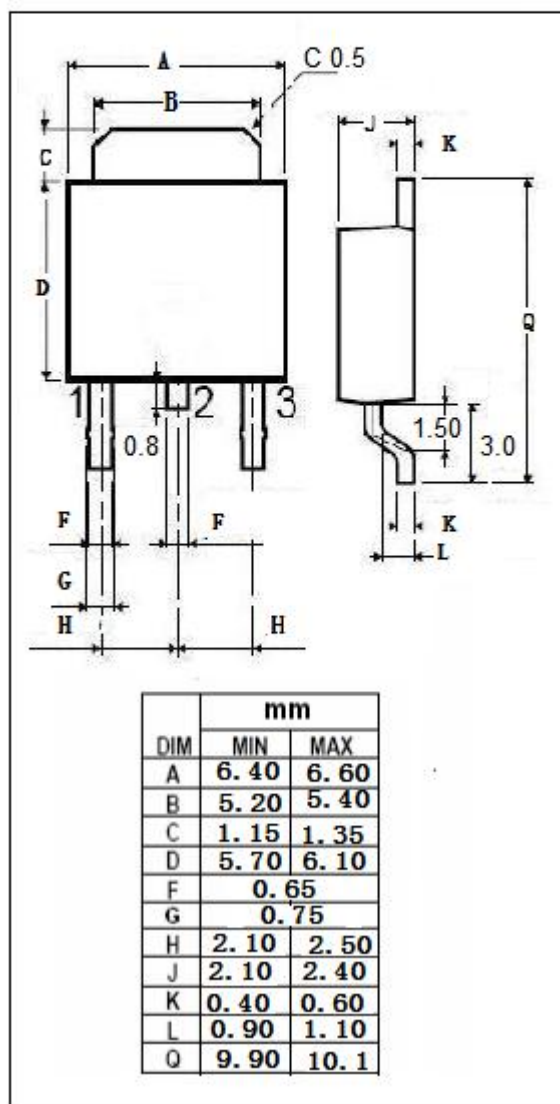
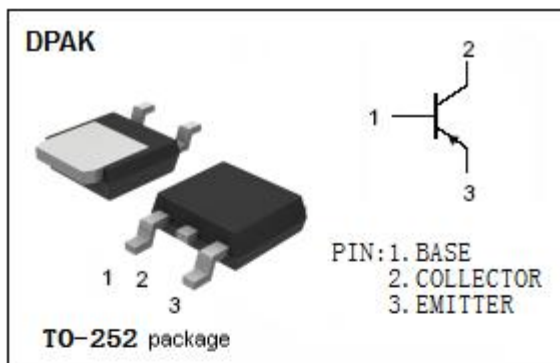
- Suitable for middle power drivers
- Low $V_{CE(sat)}$
 $V_{CE(sat)} \leq -0.4V @ (I_C = -1A, I_B = -50mA)$
- Complementary NPN types: 2SCR573D
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Low frequency amplifier

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-50	V
V_{CEO}	Collector-Emitter Voltage	-50	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current-Continuous	-3	A
I_{CM}	Collector Current-Peak	-6	A
P_C	Collector Power Dissipation @ $T_C=25^\circ C$	10	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55~150	$^\circ C$



isc Silicon PNP Power Transistor**2SAR573D****ELECTRICAL CHARACTERISTICS**T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
BV _{CBO}	Collector-Base breakdown voltage	I _C =-100uA	-50			V
BV _{CEO}	Collector-Emitter breakdown voltage	I _C =-1mA	-50			V
BV _{EBO}	Emitter-Base breakdown voltage	I _E =-100uA	-6			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -1A; I _B = -50mA			-0.4	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -50V; I _E = 0			-1.0	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -4V; I _C = 0			-1.0	μ A
h _{FE}	DC Current Gain	I _C = -0.1A; V _{CE} = -3V	180		450	
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = -10V; f= 1.0MHz		35		pF
f _T ^{NOTE}	Current-Gain—Bandwidth Product	I _C = -0.2A; V _{CE} = -10V,f= 100MHz		300		MHz

NOTE:Pulsed

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