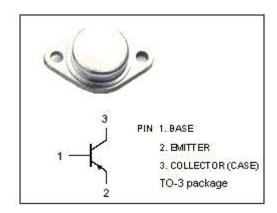


isc Silicon PNP Power Transistor

2SA882

DESCRIPTION

- High Power Dissipation-
- : P_C= 100W(Max.)@T_C=25℃
- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= -130V(Min.)
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

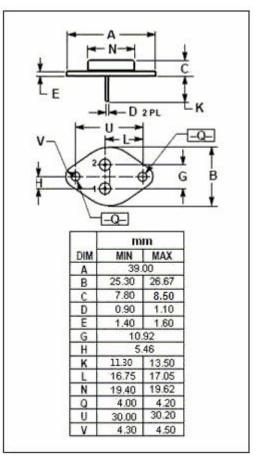


APPLICATIONS

Designed for power and switching applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{СВО}	Collector-Base Voltage	-130	V
V _{CEO}	Collector-Emitter Voltage	-130	V
V _{EBO}	Emitter-Base Voltage	-5	V
lc	Collector Current-Continuous	-7	А
Pc	Collector Power Dissipation @T _C =25°C	100	W
Tj	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature	-65~200	$^{\circ}$





isc Silicon PNP Power Transistor

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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -30mA; I _B = 0	-130			V
VcE(sat)-1	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -0.3A			-1.0	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	Ic= -7А; Iв= -1.5А			-3.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -3A; V _{CE} = -4V			-1.6	V
І _{СВО}	Collector Cutoff Current	V _{CB} = -130V; I _E = 0			-0.1	mA
ІЕВО	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-0.1	mA
h _{FE-1}	DC Current Gain	I _C = -1A; V _{CE} = -4V	40			
h _{FE-2}	DC Current Gain	I _C = -3A; V _{CE} = -4V	20			

Notice:

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