

isc Silicon PNP Power Transistor

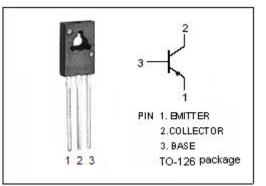
2SA738

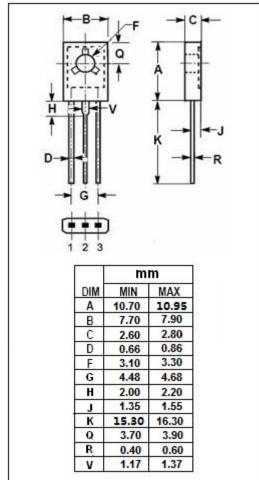
DESCRIPTION

- · Collector-Emitter Breakdown Voltage- $V_{(BR)CEO}$ = -25V (Min)
- Good Linearity of h_{FE}
- Complement to Type 2SC1368
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

· Designed for use as driver stages in high-fidelity amplifiers and TV circuits.





ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-25	V	
V _{CEO}	Collector-Emitter Voltage	-25	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
lc	Collector Current-Continuous	-2.5	A	
Ісм	Collector Current-Peak	-3.0	A	
Pc	Total Power Dissipation @ T _C =25℃	8	W	
TJ	Junction Temperature	150	°C	
T _{stg}	Storage Temperature Range	-55~150	°C	



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ELECTRICAL CHARACTERISTICS

$T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA; I _B = 0	-25			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -500mA; I _B = -50mA			-0.5	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -500mA; V _{CE} = -2V			-1.0	V
Ісво	Collector Cutoff Current	V _{CB} = -25V; I _E = 0			-1.0	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-1.0	μA
h _{FE}	DC Current Gain	I _C = -500mA; V _{CE} = -2V	60		320	
f⊤	Current-Gain—Bandwidth Product	I _C = -50mA; V _{CE} = -5V		160		MHz

Notice:

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