

## isc Silicon PNP Power Transistor

# 2SA652

### DESCRIPTION

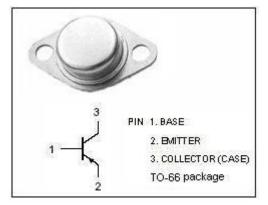
- Collector-Emitter Breakdown Voltage-: V<sub>(BR)CEO</sub>= -100V(Min.)
- Contunuous Collector Current  $I_C$ = -1A
- Power DissipationPc= 15W @Tc= 25  $^\circ\!\mathrm{C}$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

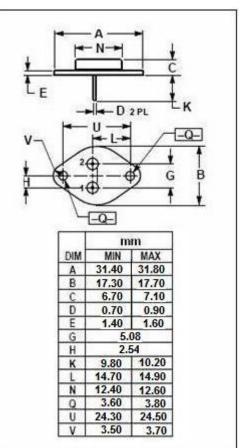
### APPLICATIONS

• Designed for low frequency power amplifier color TV vertical deflection output applications.

### ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>сво</sub>	Collector-Base Voltage	-150	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-100	V
V <sub>EBO</sub>	Emitter-Base Voltage	-6	V
Ι <sub>C</sub>	Collector Current-Continuous	-1.0	А
Pc	Collector Power Dissipation@Tc=25°C	15	W
TJ	Junction Temperature	150	°C
T <sub>stg</sub>	Storage Temperature	-55~150	°C







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### **ELECTRICAL CHARACTERISTICS**

#### $T_{\text{C}}\text{=}25^{\circ}\!\!\!\mathrm{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -50mA ; I <sub>B</sub> = 0	-100			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	Ic= -1mA; I <sub>E</sub> = 0	-150			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	Ic= -0.5A; I <sub>B</sub> = -50mA			-1.5	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -0.5A; I <sub>B</sub> = -50mA			-2.0	V
Ісво	Collector Cutoff Current	V <sub>CB</sub> = -150V; I <sub>E</sub> = 0			-10	μA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-10	μA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -0.2A; V <sub>CE</sub> = -5V	40		200	
Сов	Collector Output Capacitance	I <sub>E</sub> =0; V <sub>CB</sub> = -5V; f= 1MHz		100		pF
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> = -0.1A; V <sub>CE</sub> = -10V		15		MHz

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