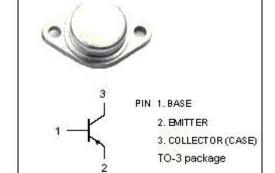
isc Silicon PNP Power Transistor

2SA650

DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= -150V(Min.)
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

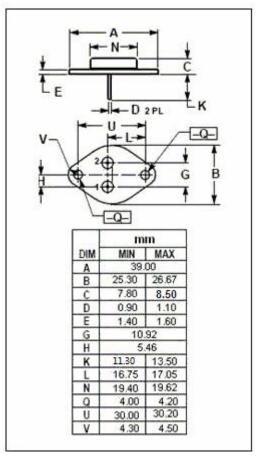


APPLICATIONS

• Designed for audio power amplifier applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-150	V	
Vceo	Collector-Emitter Voltage	-150	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
Ic	Collector Current-Continuous	-10	А	
Pc	Collector Power Dissipation @T _C =25°C	100	W	
T _j	Junction Temperature	150	$^{\circ}$ C	
T _{stg}	Storage Temperature	-55~150	$^{\circ}$	





isc Silicon PNP Power Transistor

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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -1mA; I _E = 0	-150			
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -25mA; I _B = 0	-150			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -1mA; I _C = 0	-5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A			-2.0	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A			-2.5	V
Ісво	Collector Cutoff Current	V _{CB} = -150V; I _E = 0			-0.1	mA
ІЕВО	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-0.1	mA
h _{FE}	DC Current Gain	I _C = -2A; V _{CE} = -5V	30		150	
Сов	Collector Output Capacitance	I _E =0; V _{CB} = -5V; f= 1MHz		500		pF
f⊤	Current-Gain—Bandwidth Product	I _C = -1A; V _{CE} = -5V		10		MHz

Notice:

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