

isc Silicon PNP Power Transistor

2SA1096

DESCRIPTION

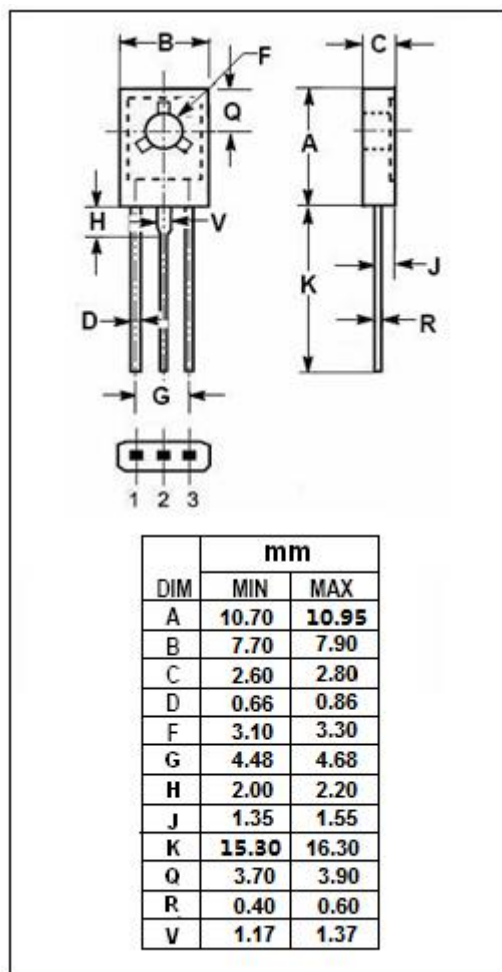
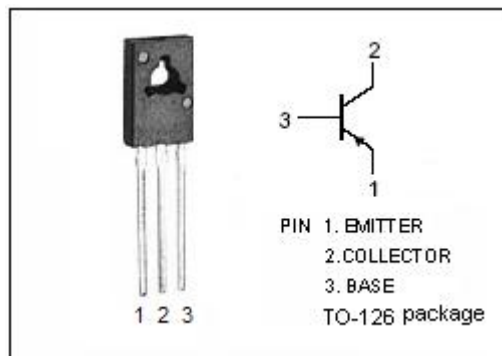
- High Collector-Emitter Breakdown Voltage-
 $V_{(BR)CEO} = -50V$ (Min)
- Good Linearity of h_{FE}
- Complement to Type 2SC2497
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low-frequency power amplification

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-70	V
V_{CEO}	Collector-Emitter Voltage	-50	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-2	A
I_{CM}	Collector Current-Peak	-3	A
P_C	Collector Power Dissipation @ $T_a=25^{\circ}C$	1.2	W
	Total Power Dissipation @ $T_C=25^{\circ}C$	5	
T_J	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}C$



isc Silicon PNP Power Transistor**2SA1096****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -2mA; I _B = 0	-50			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -1mA; I _E = 0	-70			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -1.5A; I _B = -0.15A			-1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -1.5A; I _B = -0.15A			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -20V; I _E = 0			-1	μA
I _{CEO}	Collector Cutoff Current	V _{CE} = -10V; I _B = 0			-100	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-10	μA
h _{FE}	DC Current Gain	I _C = -1A; V _{CE} = -5V	80		220	
f _T	Current-Gain—Bandwidth Product	I _E = 0.5A; V _{CB} = -5V; f = 200MHz		150		MHz
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = -20V; f = 1.0MHz		55		pF

◆ h_{FE} Classifications

Q	R
80-160	120-220

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