

## isc Silicon PNP Power Transistor

## 2SA1067

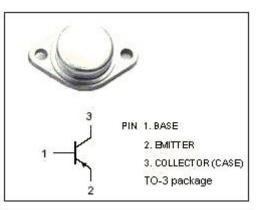
#### DESCRIPTION

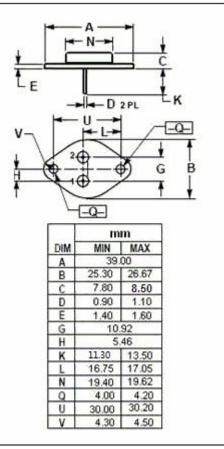
- · Collector-Emitter Breakdown Voltage-
- : V<sub>(BR)CEO</sub>= -120V(Min.)
- Good Linearity of h<sub>FE</sub>
- · Wide Area of Safe Operation
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

#### **APPLICATIONS**

• Designed for AF amplifier, high power amplifier applications.







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## ELECTRICAL CHARACTERISTICS

#### Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10mA; I <sub>B</sub> = 0	-120			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -5Α; I <sub>B</sub> = -0.5Α			-2.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -5A; I <sub>B</sub> = -0.5A			-2.5	V
Ісво	Collector Cutoff Current	V <sub>CB</sub> = -120V; I <sub>E</sub> = 0			-0.1	mA
Іево	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-0.1	mA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -3A; V <sub>CE</sub> = -4V	30		200	
f⊤	Current-Gain—Bandwidth Product	Ic= -0.5A; Vce= -10V		50		MHz

#### NOTICE:

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