

isc Silicon NPN Power Transistor

DESCRIPTION

- · Excellent Safe Operating Area
- · Low Collector-Emitter Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

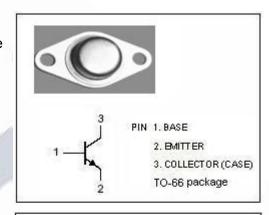
 Designed for switching regulator applications where high frequency and high voltage swings and required

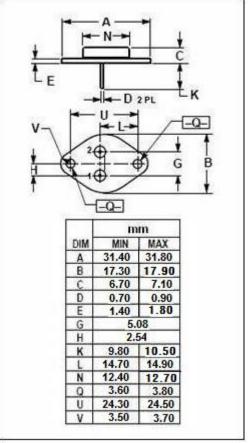
ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage		V	
V _{CEO}	Collector-Emitter Voltage	140	V	
V _{EBO}	Emitter-Base Voltage 9		V	
lc	Collector Current-Continuous 2.5		A	
Pc	Collector Power Dissipation@T _C =25℃	25	W	
TJ	Junction Temperature	-65~200	${\mathbb C}$	
T _{stg}	Storage Temperature	-65~200	$^{\circ}$	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance,Junction to Case	7.0	°C/W







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2N4273

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEO(SUS)} *	Collector-Emitter Sustaining Voltage	I _C =200mA; I _B = 0	140		V
I _{CEO}	Collector Cutoff Current	V _{CE} =140V;I _B = 0		0.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 9V; I _C = 0		0.5	mA
h _{FE} *	DC Current Gain	I _C = 1A; V _{CE} = 10V	20	140	

^{*:}Pulse test:Pulse width=300us,duty cycle≤2%

NOTICE:

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