Small Signal Switching Transistor

PNP Silicon

Features

- MIL-PRF-19500/512 Qualified
- Available as JAN, JANTX, and JANTXV

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	V _{CEO}	-80	Vdc	
Collector-Base Voltage	V _{CBO}	-80	Vdc	
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc	
Collector Current – Continuous	Ι _C	1	Adc	
Total Device Dissipation @ T _A = 25°C 2N4029 2N4033	P _T	0.5 0.8	W	
Total Device Dissipation @ T _C = 25°C 2N4029 2N4033	PT	1.0 4.0	W	
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +200	°C	

THERMAL CHARACTERISTICS

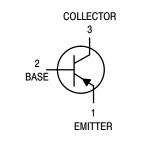
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient 2N4029 2N4033	$R_{ hetaJA}$	325 195	°C/W
Thermal Resistance, Junction-to-Case 2N4029 2N4033	$R_{ heta JC}$	150 40	°C/W

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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ORDERING INFORMATION

Level	Device	Package	Shipping
JAN JANTX JANTXV	2N4029	TO-18	Bulk
	2N4033	TO-39	Bulk

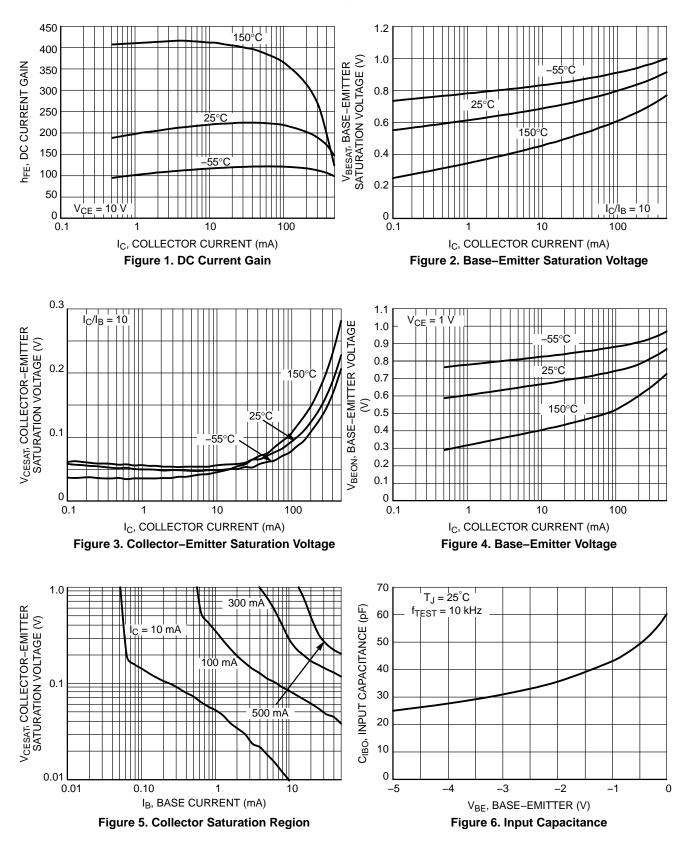
2N4029, 2N4033

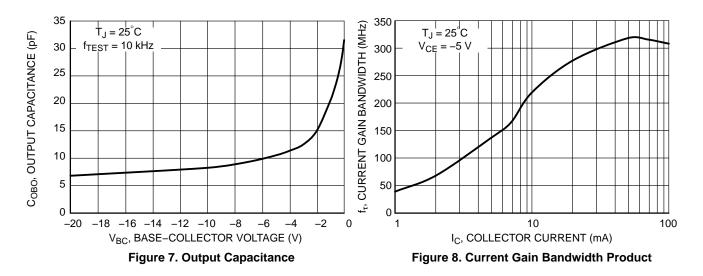
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	•	•	•	•
Collector – Emitter Breakdown Voltage $(I_C = -10 \text{ mAdc})$	V _{(BR)CEO}	-80	-	Vdc
Collector – Emitter Cutoff Current ($V_{CE} = -60 \text{ Vdc}$)	ICES	_	-25	nAdc
Collector–Base Cutoff Current $(V_{CB} = -80 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -60 \text{ Vdc}, I_E = 0)$	Ісво		-10 -10	μA nA
Emitter–Base Cutoff Current $(V_{EB} = -5 \text{ Vdc})$ $(V_{EB} = -3 \text{ Vdc})$	I _{EBO}		-10 -25	μA nA
ON CHARACTERISTICS (Note 1)	·			•
	h _{FE}	50 100 70 25	 300 	-
Collector – Emitter Saturation Voltage ($I_C = -150 \text{ mAdc}, I_B = -15 \text{ mAdc}$) ($I_C = -500 \text{ mAdc}, I_B = -50 \text{ mAdc}$) ($I_C = -1 \text{ Adc}, I_B = -100 \text{ mAdc}$)	V _{CE(sat)}	- - -	-0.15 -0.5 -1.0	Vdc
Base – Emitter Saturation Voltage ($I_C = -150 \text{ mAdc}$, $I_B = -15 \text{ mAdc}$) ($I_C = -500 \text{ mAdc}$, $I_B = -50 \text{ mAdc}$)	V _{BE(sat)}		-0.9 -1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS	·			•
Magnitude of Small–Signal Current Gain ($I_C = -50$ mAdc, $V_{CE} = -10$ Vdc, f = 100 MHz)	h _{fe}	1.5	6.0	-
Output Capacitance ($V_{CB} = -10$ Vdc, $I_E = 0$, 100 kHz $\leq f \leq 1.0$ MHz)	C _{obo}	_	20	pF
Input Capacitance (V _{EB} = -0.5 Vdc, I _C = 0, 100 kHz \leq f \leq 1.0 MHz)	C _{ibo}	_	80	pF
SWITCHING CHARACTERISTICS				
Delay Time (Reference Figure in MIL-PRF-19500/512)	t _d	-	15	ns
Rise Time (Reference Figure in MIL-PRF-19500/512)	tr	_	25	ns
Storage Time (Reference Figure in MIL-PRF-19500/512)	t _s	_	175	ns
Fall Time (Reference Figure in MIL-PRF-19500/512)	t _f	-	35	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 1. Pulse Test: Pulse Width = $300 \ \mu$ s, Duty Cycle $\leq 2.0\%$.

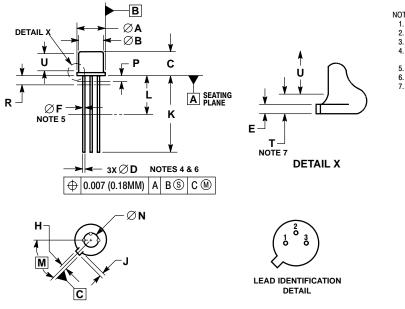
2N4029, 2N4033

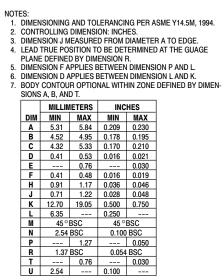




PACKAGE DIMENSIONS

TO-18 3 CASE 206AA **ISSUE A**



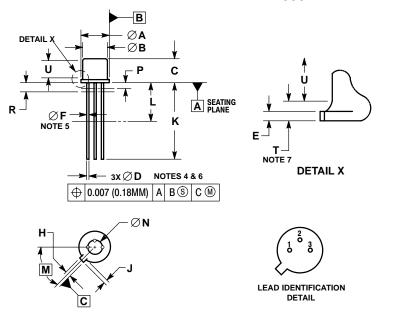


STYLE 1: PIN 1. EMITTER BASE

2. 3. COLLECTOR

PACKAGE DIMENSIONS

TO-39 3-Lead CASE 205AB **ISSUE A**



NOTES

- LO. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: INCHES. DIMENSION J MEASURED FROM DIAMETER A TO EDGE. LEAD TRUE POSITION TO BE DETERMINED AT THE GUAGE 2
- 3
- 4 PLANE DEFINED BY DIMENSION R.
- DIMENSION F APPLIES BETWEEN DIMENSION P AND L. DIMENSION D APPLIES BETWEEN DIMENSION L AND K. 5. 6.
- 7. BODY CONTOUR OPTIONAL WITHIN ZONE DEFINED BY DIMEN-SIONS A, B, AND T. 8 DIMENSION B SHALL NOT VARY MORE THAN 0.010 IN ZONE P.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	8.89	9.40	0.350	0.370
В	8.00	8.51	0.315	0.335
C	6.10	6.60	0.240	0.260
D	0.41	0.48	0.016	0.019
E	0.23	3.18	0.009	0.125
F	0.41	0.48	0.016	0.019
Н	0.71	0.86	0.028	0.034
J	0.73	1.02	0.029	0.040
K	12.70	14.73	0.500	0.580
L	6.35		0.250	
М	45°BSC		45 °BSC	
Ν	5.08 BSC		0.200 BSC	
Ρ		1.27		0.050
R	1.37 BSC		0.054 BSC	
Т		0.76		0.030
U	2.54		0.100	

STYLE 1:

PIN 1. EMITTER BASE 2.

3. COLLECTOR

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