

isc Silicon NPN Power Transistor

2N3667

DESCRIPTION

- Excellent Safe Operating Area
- Low Collector-Emitter Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

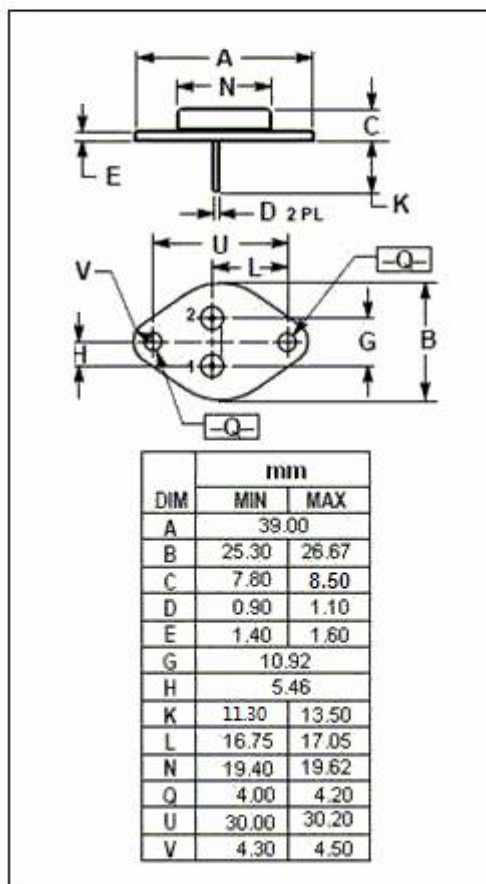
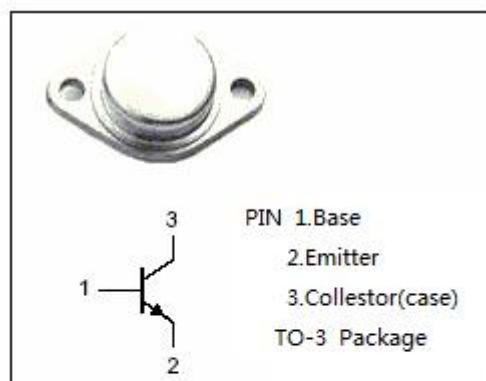
- Designed for general purpose high power switch and amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	50	V
V_{CEO}	Collector-Emitter Voltage	50	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	15	A
P_C	Collector Power Dissipation@ $T_C=25^\circ\text{C}$	117	W
T_J	Junction Temperature	200	$^\circ\text{C}$
T_{stg}	Storage Temperature	-65~200	$^\circ\text{C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	1.17	$^\circ\text{C/W}$



isc Silicon NPN Power Transistor**2N3667****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CEQ(SUS)}	Collector-Emitter Sustaining Voltage	I _C =10mA; I _B = 0	50		V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 0.5A		1.1	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 5A; V _{CE} = 4V		1.8	V
h _{FE}	DC Current Gain	I _C = 5A; V _{CE} = 4V	15	60	
f _T	Current Gain-Bandwidth Product	I _C = 0.5A; V _{CE} = 4V; f= 1.0MHz	0.5		MHz

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