

isc Silicon PNP Power Transistor

2N3184

DESCRIPTION

- Excellent Safe Operating Area
- With TO-3 package
- Low collector saturation voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

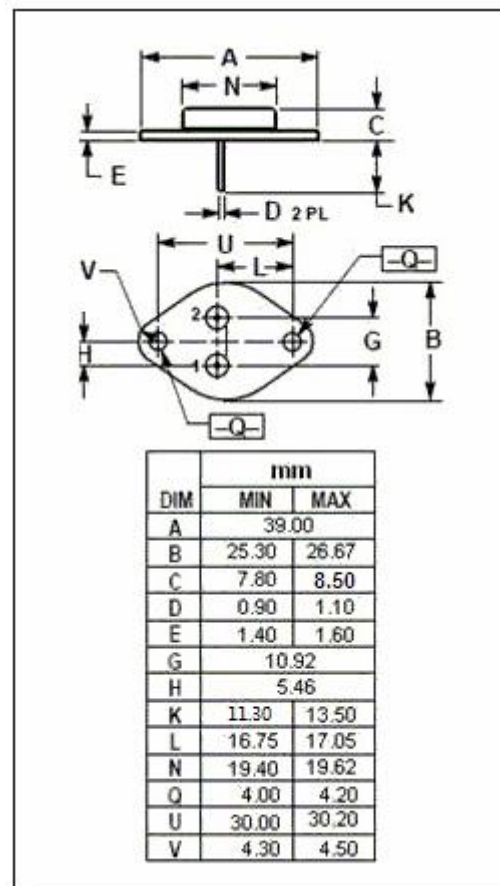
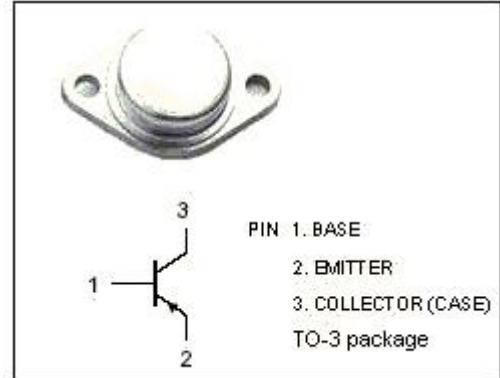
- For medium-speed switching and amplifier applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-80	V
V_{CEO}	Collector-Emitter Voltage	-80	V
V_{EBO}	Emitter-Base Voltage	-10	V
I_C	Collector Current-Continuous	-5	A
P_C	Collector Power Dissipation@ $T_C=25^{\circ}\text{C}$	75	W
T_J, T_{stg}	Operating and Storage Junction Temperature Range	-65~+200	$^{\circ}\text{C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	1.17	$^{\circ}\text{C/W}$



isc Silicon PNP Power Transistors**2N3184****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -2A; I _B = -0.3A		-1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -2A; V _{CE} =-0.3V		-2.0	V
I _{CEO}	Collector Cutoff Current	V _{CE} = -80V; I _B =0		-5.0	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C =0		-1.0	mA
h _{FE}	DC Current Gain	I _C = -2A ; V _{CE} = -3V	10	30	

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