

FAST SOFT-RECOVERY RECTIFIER DIODES

Silicon diodes in DO-5 metal envelopes, featuring non-snap-off characteristics. They are intended for use in high-frequency power supplies, thyristor inverters and multi-phase power rectifier applications. The series consists of the following types:

Normal polarity (cathode to stud): 1N3909, 1N3910, 1N3911, 1N3912, 1N3913.

Reverse polarity (anode to stud): 1N3909R, 1N3910R, 1N3911R, 1N3912R, 1N3913R.

QUICK REFERENCE DATA

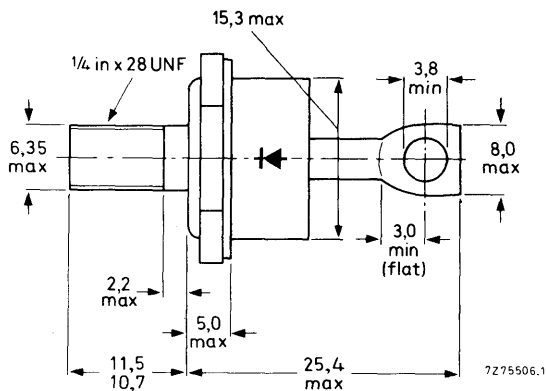
		1N3909(R)	3910(R)	3911(R)	3912(R)	3913(R)	
Repetitive peak reverse voltage	V_{RRM} max.	50	100	200	300	400	V
Average forward current	$I_F(AV)$ max.			30			A
Non-repetitive peak forward current	I_{FSM} max.			300			A
Reverse recovery time	t_{rr} <			200			ns

MECHANICAL DATA

Dimensions in mm

Fig. 1 DO-5; Supplied with device: 1 nut, 1 lock-washer

Nut dimensions across the flats: 11.1 mm



Net mass: 22 g

Diameter of clearance hole: max. 6.5 mm

Accessories supplied on request:

56264A (mica washer, insulating ring, tag)

The mark shown applies to normal polarity types.

Torque on nut:

min. 1.7 Nm (17 kg cm)

max. 2.5 Nm (25 kg cm)

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Voltages

			1N3909(R)	3910(R)	3911(R)	3912(R)	3913(R)	
Non-repetitive peak reverse voltage ($t = 10$ ms)	V_{RSM}	max.	75	200	300	400	500	V
Repetitive peak reverse voltage ($\delta \leq 0.01$)	V_{RRM}	max.	50	100	200	300	400	V
Crest working voltage	V_{RWM}	max.	50	100	200	300	400	V

Currents

Average on-state current assuming zero switching losses (averaged over any 20 ms period)

up to $T_{mb} = 100$ °Cat $T_{mb} = 125$ °C

$I_F(AV)$	max.	30	A
$I_F(AV)$	max.	15	A

R.M.S. forward current

$I_F(RMS)$	max.	45	A
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Repetitive peak forward current

I_{FRM}	max.	125	A
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Non-repetitive peak forward current

 $T_j = 150$ °C prior to surge;half sine-wave with reapplied V_{RWMmax} ; $t = 10$ ms $t = 8.3$ ms

I_{FSM}	max.	275	A
I_{FSM}	max.	300	A

 I^2t for fusing ($t = 10$ ms)

I^2t	max.	375	A ² s
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Temperatures

Storage temperature

T_{stg}	-65 to 175	°C
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Operating junction temperature

T_j	max. 150	°C
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THERMAL RESISTANCE

From junction to mounting base

$R_{th j-mb}$	=	1.0	°C/W
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From mounting base to heatsink
with heatsink compound

$R_{th mb-h}$	=	0.3	°C/W
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Transient thermal impedance; $t = 1$ ms

$Z_{th j-mb}$	=	0.2	°C/W
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CHARACTERISTICS

Forward voltage				
$I_F = 30\text{ A}; T_j = 25\text{ }^{\circ}\text{C}$	V_F	<	1.4	V*
Reverse current				
$V_R = V_{RWMmax}; T_j = 100\text{ }^{\circ}\text{C}$	I_R	<	10	mA
Reverse recovery when switched from				
$I_F = 1\text{ A to } V_R \geq 30\text{ V}; -dI_F/dt = 35\text{ A}/\mu\text{s}; T_j = 25\text{ }^{\circ}\text{C}$				
Recovery time	t_{rr}	<	200	ns
$I_F = 2\text{ A to } V_R \geq 30\text{ V}; -dI_F/dt = 20\text{ A}/\mu\text{s}; T_j = 25\text{ }^{\circ}\text{C}$				
Recovered charge	Q_s	<	250	nC
Maximum slope of the reverse recovery current				
when switched from $I_F = 1\text{ A to } V_R \geq 30\text{ V}; -dI_F/dt = 2\text{ A}/\mu\text{s}; T_j = 25\text{ }^{\circ}\text{C}$	$ dI_R/dt $	<	5	A/ μs

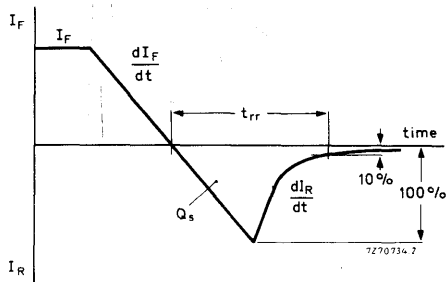


Fig. 2 Definitions of t_{rr} and Q_s .

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*Measured under pulse conditions to avoid excessive dissipation.

SINUSOIDAL OPERATION

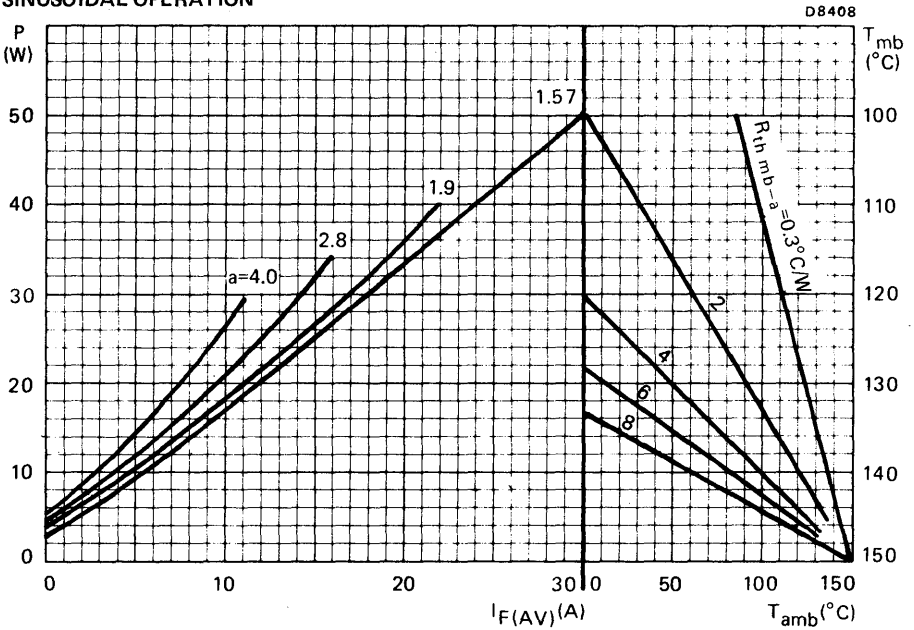


Fig. 3 The right-hand part shows the interrelationship between the power (derived from the left-hand part) and the maximum permissible temperatures.
 P = power dissipation excluding switching losses.
 a = form factor = $I_{F(RMS)}/I_{F(AV)}$.

SQUARE-WAVE OPERATION

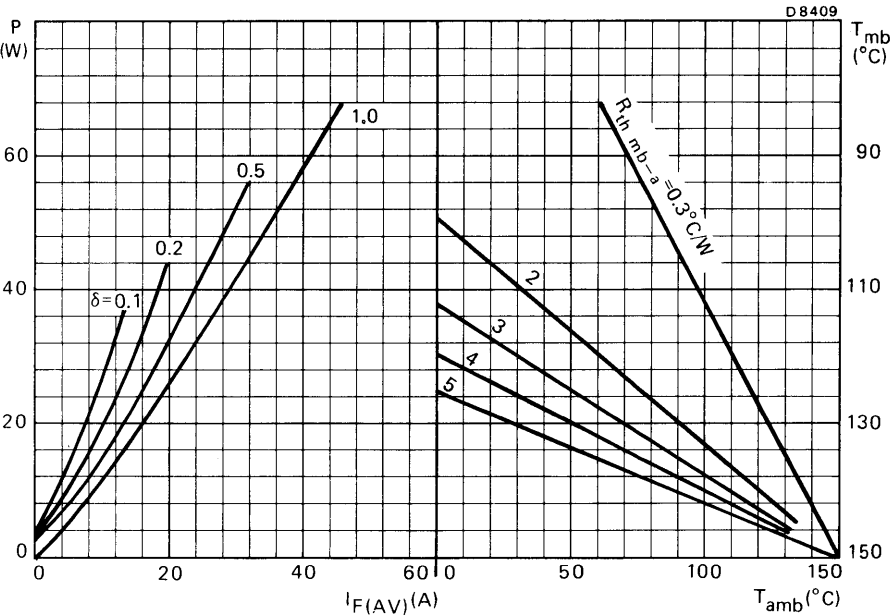
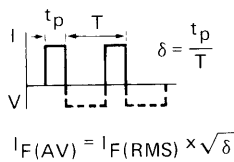


Fig. 4 The right-hand part shows the interrelationship between the power (derived from the left-hand part) and the maximum permissible temperatures.
 P = power dissipation excluding switching losses.



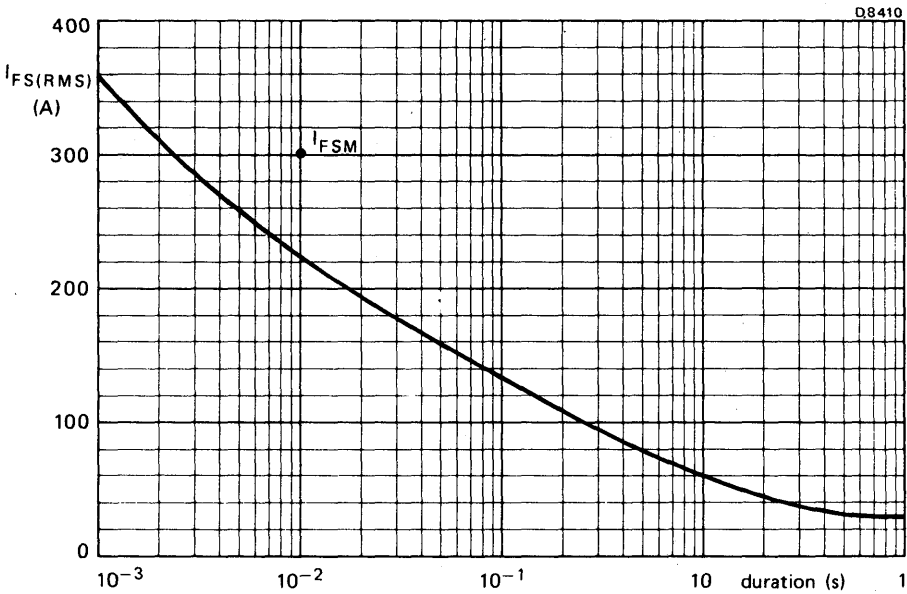


Fig.5 Maximum permissible non-repetitive r.m.s. forward current based on sinusoidal currents ($f = 50 \text{ Hz}$); $T_j = 150^\circ\text{C}$ prior to surge; with reapplied V_{RWMmax} .



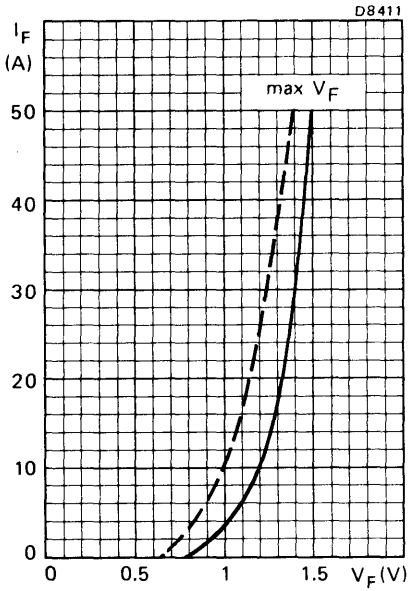


Fig. 6 — $T_j = 25\text{ }^{\circ}\text{C}$; --- $T_j = 150\text{ }^{\circ}\text{C}$

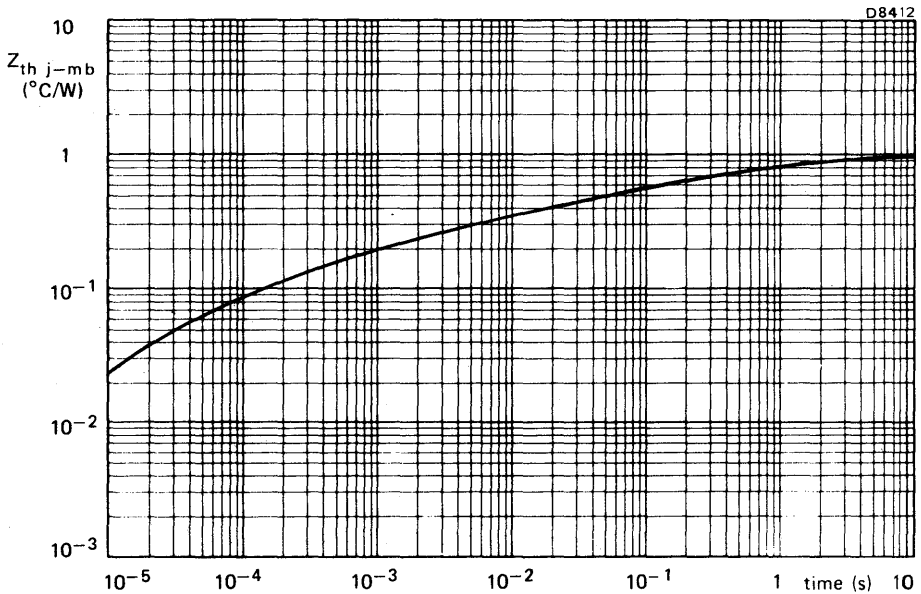


Fig. 7