

Major Ratings and Characteristics

Characteristics	Values	Units
$I_{F(AV)}$ Rectangular waveform	180	A
V_{RRM}	45	V
I_{FSM} @ $t_p=5\mu s$ sine	27000	A
V_F @180Apk, $T_J=125^\circ$	0.63	V
T_J range	-55 to 150	$^\circ C$

Description/ Features

The 180NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 $^\circ C$ junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, free-wheeling diodes, welding, and reverse battery protection.

- 150 $^\circ C$ T_J operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead-Free

Case Styles



HALF-PAK (D-67)

180NQ045PbF

Bulletin PD-21146 06/06

International
IOR Rectifier

Voltage Ratings

Part number	180NQ045PbF
V_R Max DC Reverse Voltage (V)	45
V_{RM} Max Working Peak Reverse Voltage (V)	

Absolute Maximum Ratings

Parameters	180NQ	Units	Conditions
$I_{F(AV)}$ Max Average Forward Current *See Fig. 5	180	A	50% duty cycle @ $T_C = 105^\circ\text{C}$, rectangular wave form
I_{FSM} Max Peak One Cycle Non-Repetitive Surge Current *See Fig. 7	27000	A	5 μs Sine or 3 μs Rect. pulse
	2400		10ms Sine or 6ms Rect. pulse
E_{AS} Non-Repetitive Avalanche Energy	243	mJ	$T_J = 25^\circ\text{C}$, $I_{AS} = 22\text{ Amps}$, $L = 1\text{ mH}$
I_{AR} Repetitive Avalanche Current	6	A	Current decaying linearly to zero in 1 μsec Frequency limited by T_J , max $V_A = 1.5 V_R$ typical

Electrical Specifications

Parameters	180NQ	Units	Conditions
V_{FM} Max Forward Voltage Drop *See Fig. 1 (1)	0.60	V	@ 180A
	0.83	V	@ 60A
	0.63	V	@ 180A
	0.89	V	@ 60A
I_{RM} Max Reverse Leakage Current *See Fig. 2	15	mA	$T_J = 25^\circ\text{C}$
	600	mA	$T_J = 125^\circ\text{C}$
C_T Max Junction Capacitance	7700	pF	$V_R = 5V_{DC}$ (test signal range 100Khz to 1Mhz) 25°C
L_S Typical Series Inductance	6.0	nH	From top of terminal hole to mounting plane
dv/dt Max Voltage Rate of Change (Rated V_R)	10000	V/ μs	

(1) Pulse width = 500 μs

Thermal-Mechanical Specifications

Parameters		180NQ	Units	Conditions
T _J	MaxJunctionTemperatureRange	-55 to 150	°C	
T _{stg}	MaxStorageTemperatureRange	-55 to 150	°C	
R _{thJC}	MaxThermalResistanceJunction toCase	0.28	°C/W	DC operation * See Fig. 4
R _{thCS}	TypicalThermalResistance, CasetoHeatsink	0.05	°C/W	Mounting surface, smooth and greased
wt	ApproximateWeight	0 (1.06) g(oz.)		
T	MountingTorque	Min.	2 (6.5)	Non-lubricated threads
		Max	4 (9.4)	
	TerminalTorque	Min.	3 (9)	
		Max	5 (44.2)	
CaseStyle		HALF PAK Module		

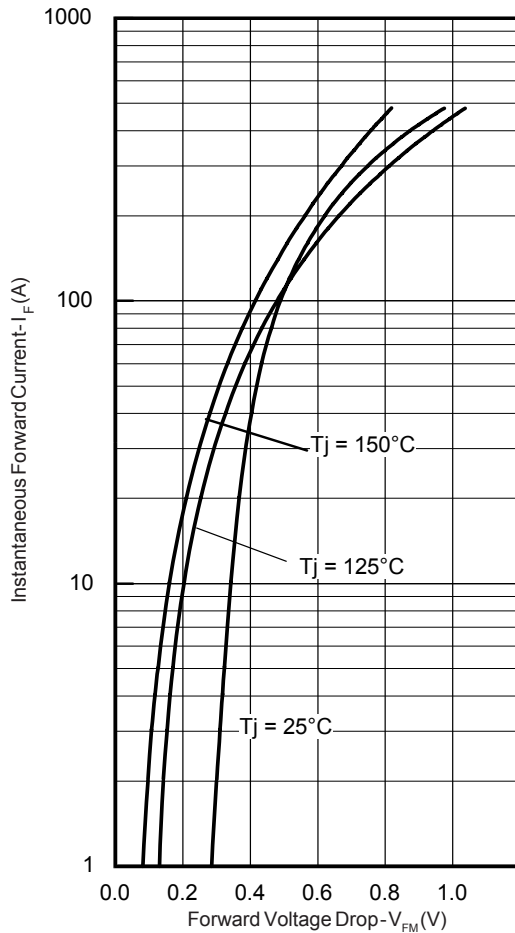


Fig. 1 - Max. Forward Voltage Drop Characteristics

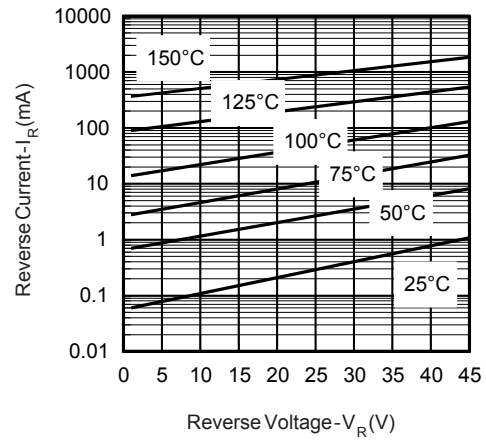


Fig. 2 - Typical Values Of Reverse Current Vs. Reverse Voltage

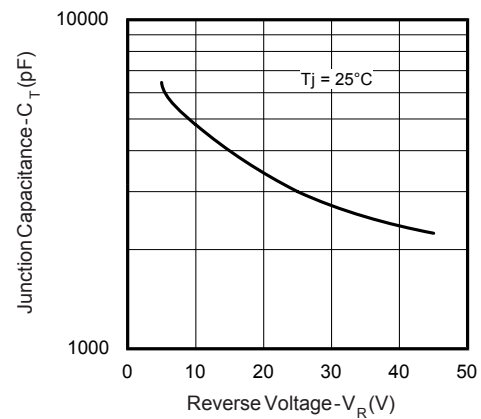


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage

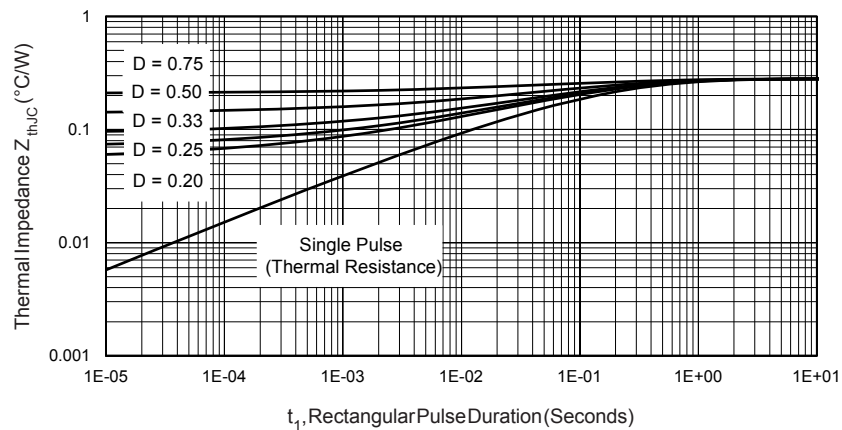


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

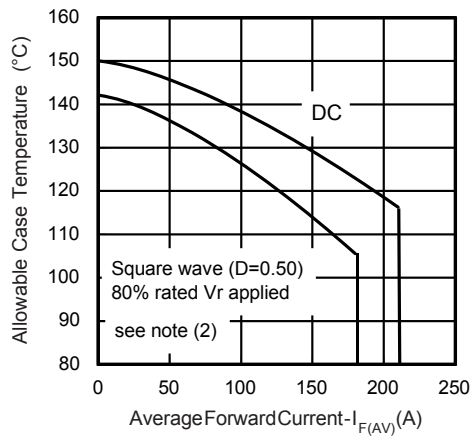


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current

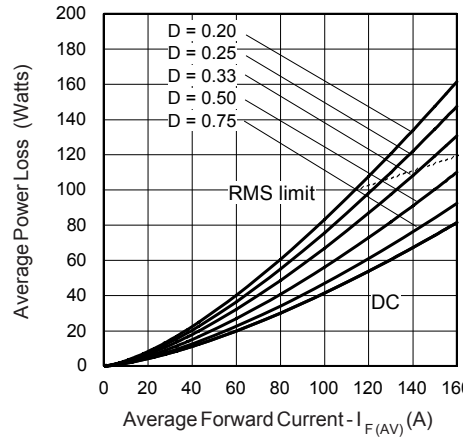


Fig. 6 - Forward Power Loss Characteristics

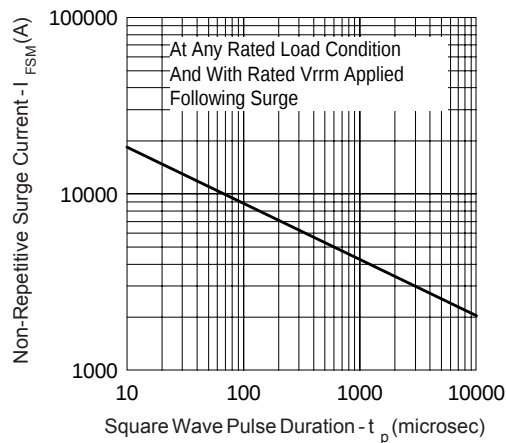


Fig. 7 - Max. Non-Repetitive Surge Current

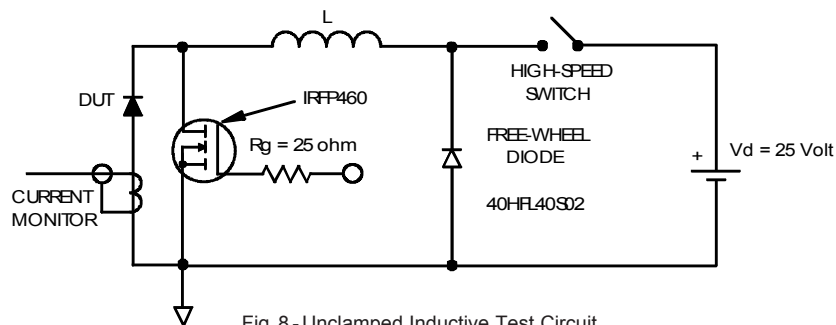


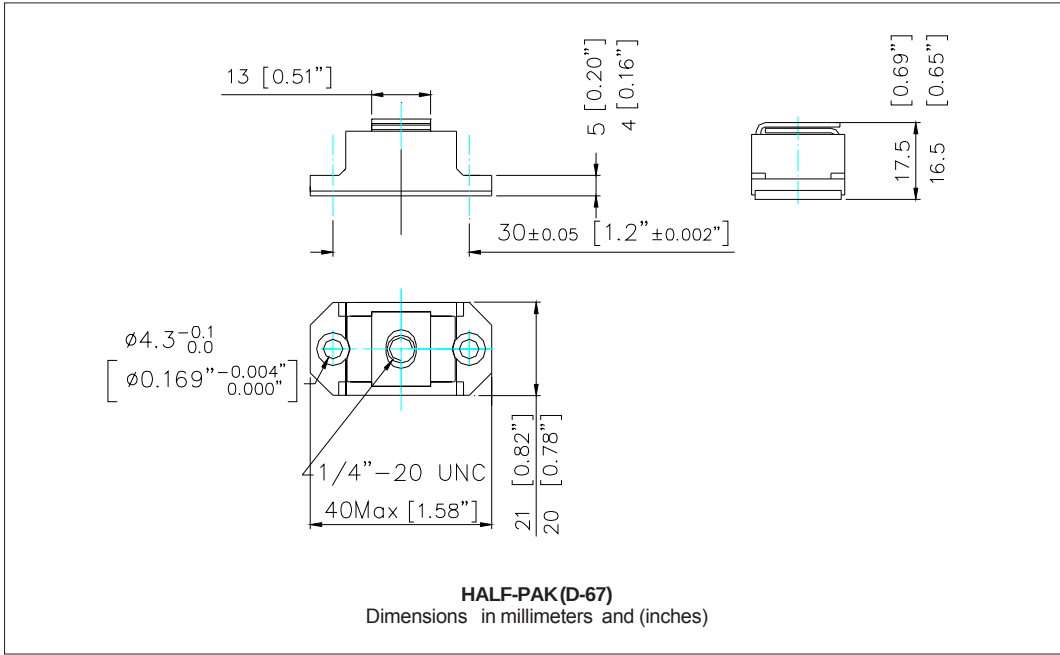
Fig. 8 - Unclamped Inductive Test Circuit

(2) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;

P_d = Forward Power Loss = $I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D)$ (see Fig. 6);

$P_{d_{REV}}$ = Inverse Power Loss = $V_{R1} \times I_R (1 - D)$; $I_R @ V_{R1}$ = rated V_R

Outline Table



Ordering Information Table

Device Code					
18	0	N	Q	045	PbF
①	②	③	④	⑤	⑥
1	-	Average Current Rating (x 10)			
2	-	Product Silicon Identification			
3	-	N = NOT Isolated			
4	-	Q = Schottky Rectifier Diode			
5	-	Voltage Rating (045 = 45V)			
6	-	Lead-Free			

180NQ045PbF

Bulletin PD-21146 06/06

International
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Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level and Lead-Free.
Qualification Standards can be found on IR's Web site.

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IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
TAC Fax: (310) 252-7309
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