



14N50-ML

Power MOSFET

14A, 500V N-CHANNEL POWER MOSFET

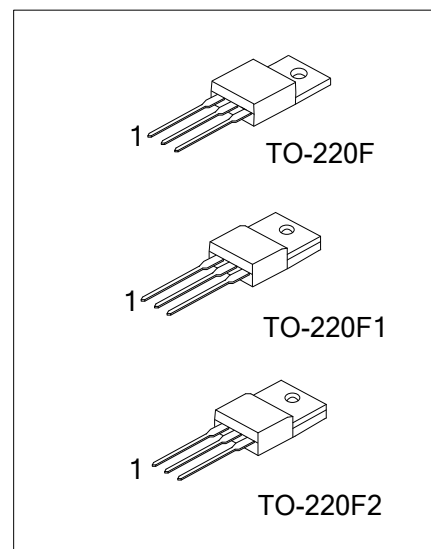
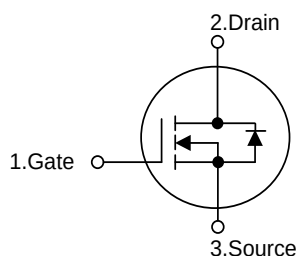
DESCRIPTION

The UTC 14N50-ML is a high voltage power MOSFET combines advanced trench MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)} \leq 0.58 \Omega @ V_{GS}=10V, I_D=7.0A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL



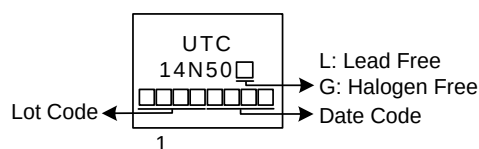
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
14N50L-TF1-T	14N50G-TF1-T	TO-220F1	G	D	S	Tube
14N50L-TF2-T	14N50G-TF2-T	TO-220F2	G	D	S	Tube
14N50L-TF3-T	14N50G-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

14N50G-TF1-T	(1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube (2) TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DSS}	500	V
Gate-Source Voltage	V _{GSS}	±30	V
Continuous Drain Current	I _D	14	A
Pulsed Drain Current (Note 2)	I _{DM}	28	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	714
Peak Diode Recovery dv/dt (Note 4)			
Power Dissipation	P _D	40	W
Junction Temperature	T _J	+150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 30mH, I_{AS} = 6.9A, V_{DD} = 100V, R_G = 25 Ω, Starting T_J = 25°C

4. I_{SD} ≤ 14A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ _{JA}	62.5	°C/W
Junction to Case	θ _{JC}	3.125	°C/W

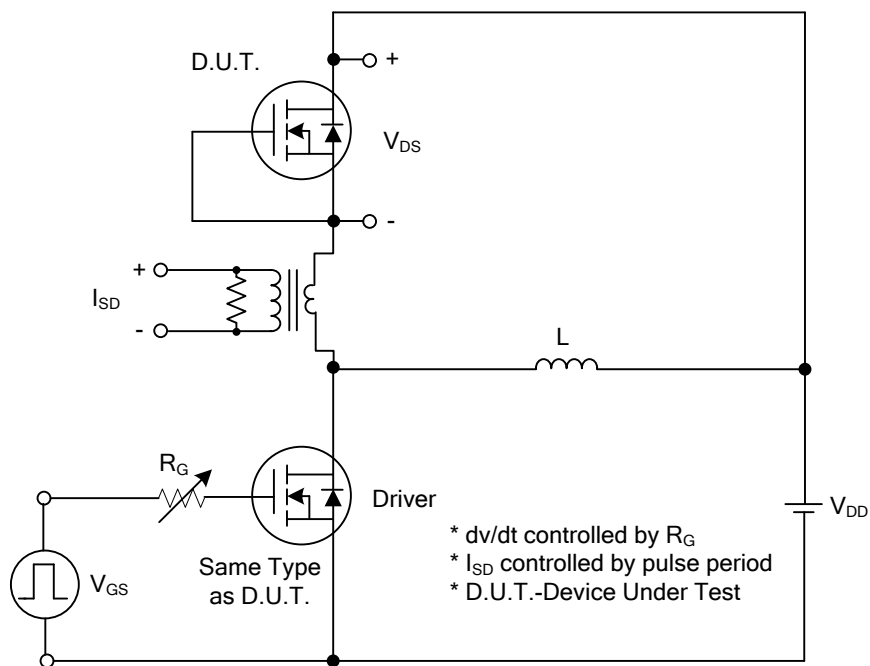
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	500			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =500V, V _{GS} =0V			10	μA
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} =30V, V _{DS} =0V			100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =7.0A			0.58	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		1540		pF
Output Capacitance		C _{OSS}			169		pF
Reverse Transfer Capacitance		C _{RSS}			15		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q _G	V _{DS} =400V, V _{GS} =10V, I _D =14A I _G =1mA (Note 1, 2)		36		nC
Gate-Source Charge		Q _{GS}			8		nC
Gate-Drain Charge		Q _{GD}			10		nC
Turn-On Delay Time (Note 1)		t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =14A, R _G =25Ω (Note 1, 2)		20		ns
Turn-On Rise Time		t _R			24		ns
Turn-Off Delay Time		t _{D(OFF)}			115		ns
Turn-Off Fall Time		t _F			30		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Body-Diode Continuous Current		I _S				14	A
Maximum Body-Diode Pulsed Current		I _{SM}				28	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =14A , V _{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)		t _{rr}	I _S =14A , V _{GS} =0V		344		ns
Reverse Recovery Charge		Q _{rr}	di/dt=100A/μs		9.1		μC

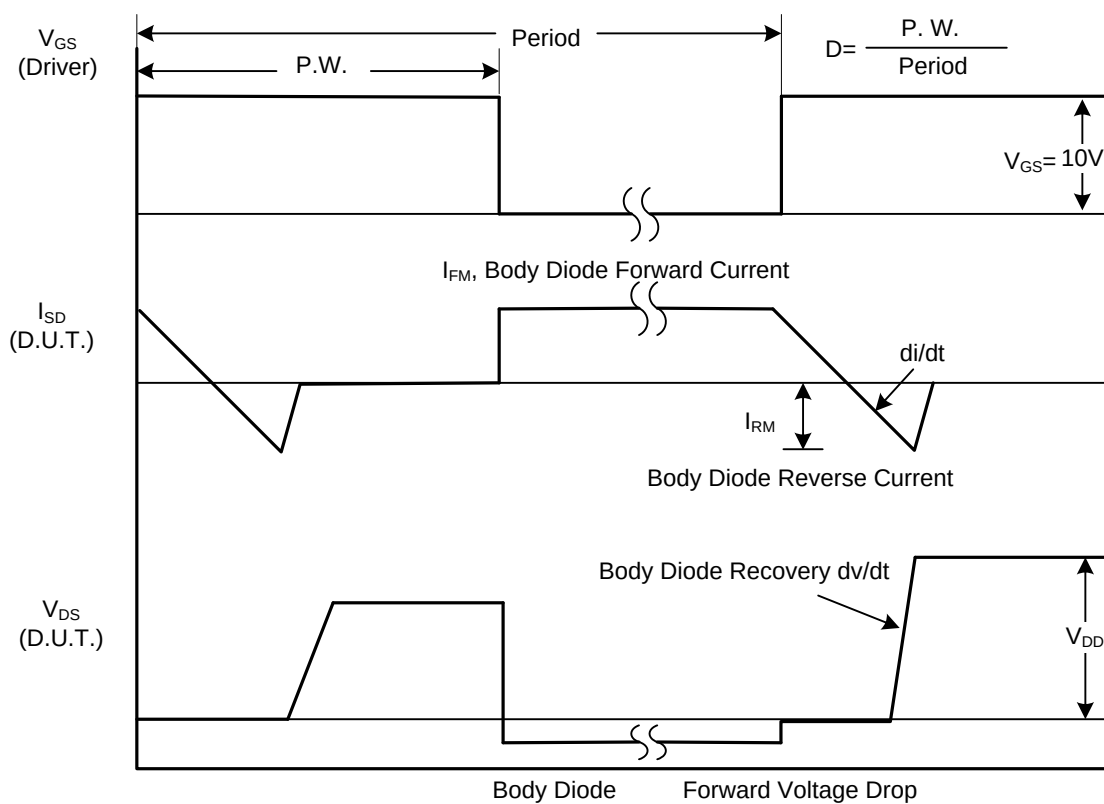
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

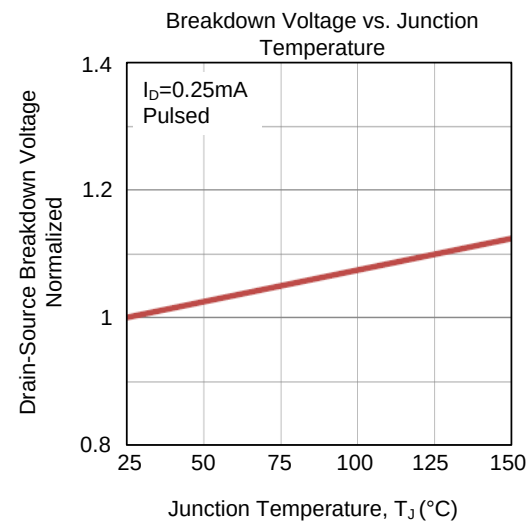
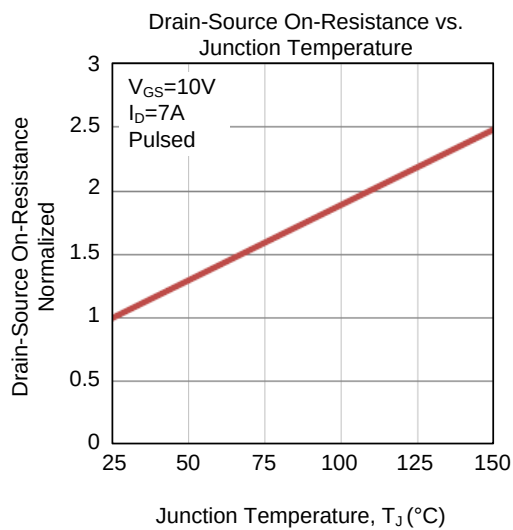
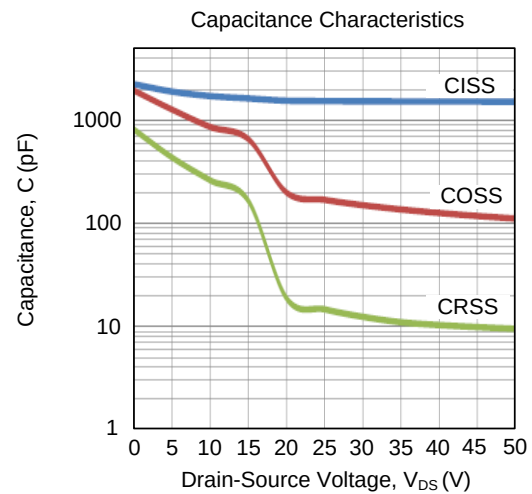
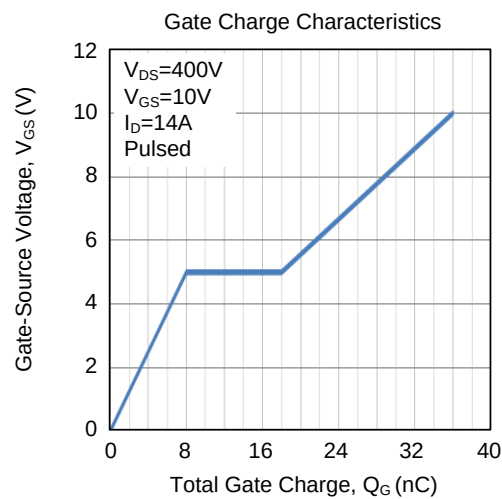
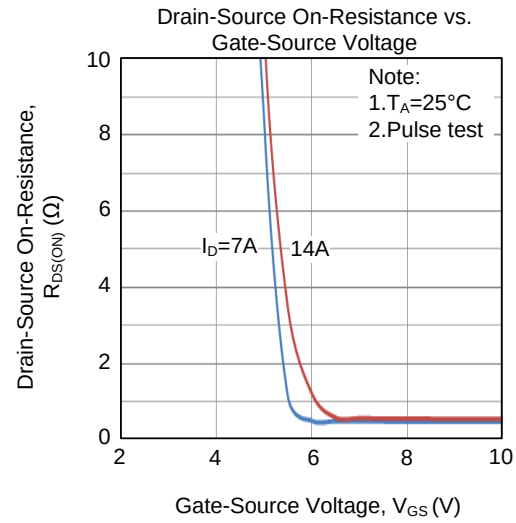
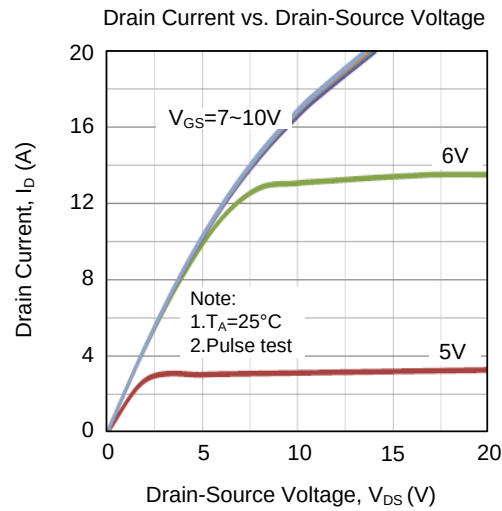
Pulse Width $\leq 1\mu\text{s}$
Duty Factor $\leq 0.1\%$

The graph shows the relationship between gate voltage V_{GS} and charge. The curve starts at the origin, rises linearly to a plateau (labeled Q_{GS}), remains constant (labeled Q_{GD}), and then rises linearly again (labeled Q_G).

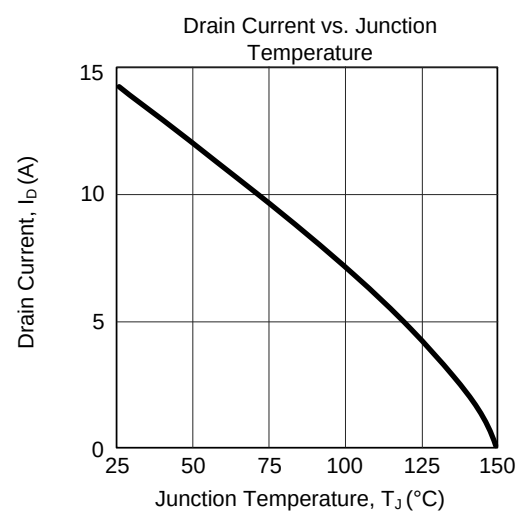
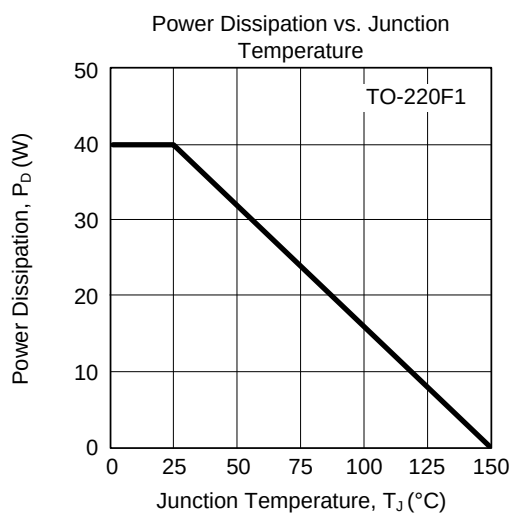
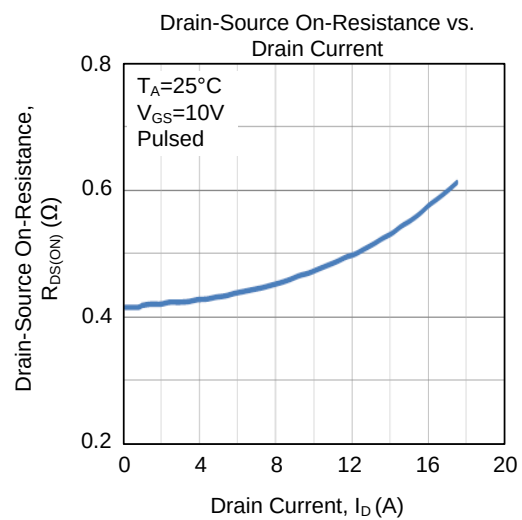
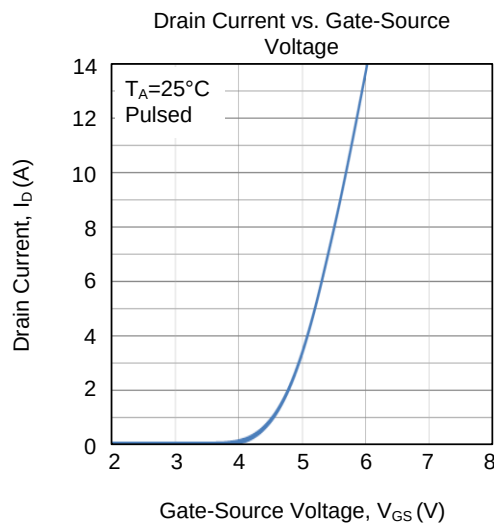
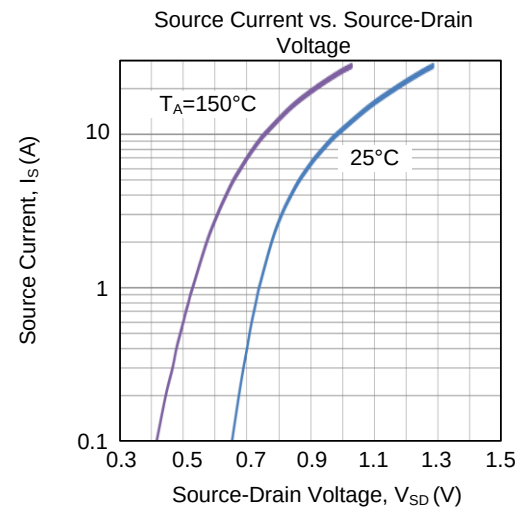
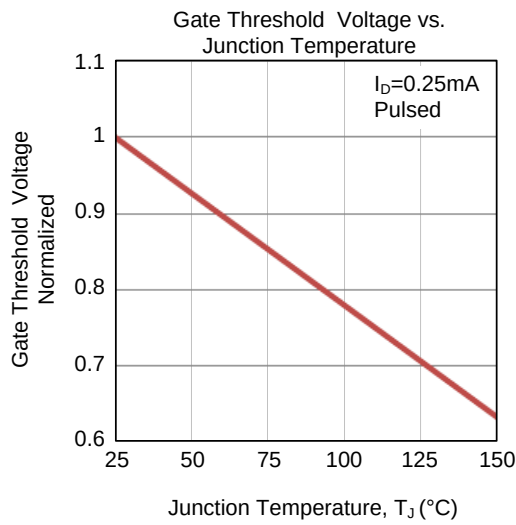


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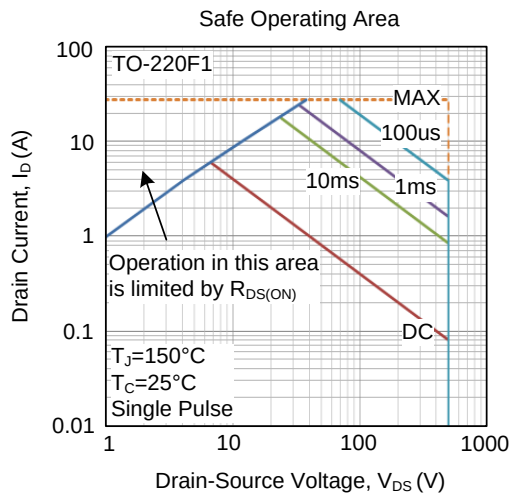
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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