

# UNISONIC TECHNOLOGIES CO., LTD

13N120-E2 Preliminary Power MOSFET

# 13A, 1200V N-CHANNEL POWER MOSFET

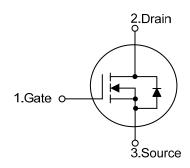
#### **■** DESCRIPTION

The UTC **13N120-E2** provide excellent  $R_{DS(ON)}$ , low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

#### **■** FEATURESO

- \*  $R_{DS(ON)} \le 1.5 \Omega$  @  $V_{GS}$ =10V,  $I_D$ =6.5A
- \* Low Reverse Transfer Capacitance
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, High Ruggedness

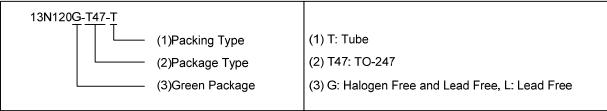
#### ■ SYMBOL



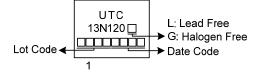
# ORDERING INFORMATION

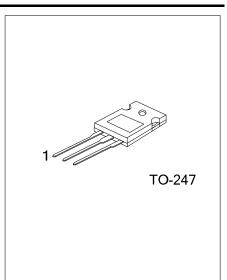
Ordering Number		Dealtage	Pin Assignment			Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
13N120L-T47-T	13N120G-T47-T	TO-247	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



#### ■ MARKING





<u>www.unisonic.com.tw</u> 1 of 5

# ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		$V_{DSS}$	1200	V	
Gate-Source Voltage		$V_{GSS}$	±30	V	
Drain Current	Continuous	$I_{D}$	13	Α	
	Pulsed (Note 2)	$I_{DM}$	26	Α	
Avalanche Energy Single Pulsed (Note 3)		E <sub>AS</sub>	367	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.09	V/ns	
Power Dissipation		$P_D$	360	W	
Junction Temperature		$T_J$	+150	°C	
Storage Temperature		$T_{STG}$	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L=30mH,  $I_{AS}$ =4.95A,  $V_{DD}$ =100V,  $R_{G}$ =25  $\Omega$ , Starting  $T_{J}$  = 25°C
- 4.  $I_{SD} \le 13A$ , di/dt  $\le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25^{\circ}C$

# **■ THERMAL DATA**

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Ambient	$\theta_{JA}$	50	°C/W	
Junction to Case	$\theta_{ m JC}$	0.34	°C/W	

# ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C, unless otherwise specified)

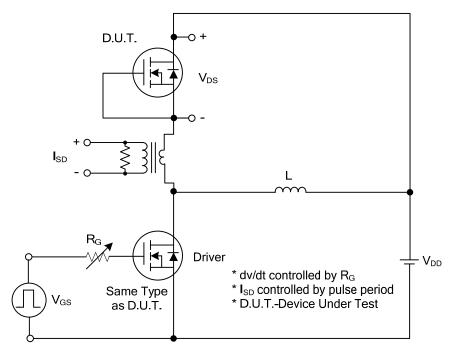
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	n Voltage BV <sub>DSS</sub> V <sub>GS</sub> =0V, I <sub>D</sub> =250µA		1200			V		
Drain-Source Leakage Current	$I_{DSS}$	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V			10	μΑ		
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}$ =±30V, $V_{DS}$ =0V			±100	nA		
ON CHARACTERISTICS								
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	3.0		5.0	V		
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, $I_D$ =6.5A		1.15	1.5	Ω		
DYNAMIC CHARACTERISTICS								
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz		2840		pF		
Output Capacitance	Coss			280		pF		
Reverse Transfer Capacitance	$C_{RSS}$			46		pF		
SWITCHING CHARACTERISTICS								
Total Gate Charge (Note 1)	$Q_G$	V <sub>DS</sub> =960V, V <sub>GS</sub> =10V, I <sub>D</sub> =13A (Note 1, 2)		105		nC		
Gate-Source Charge	$Q_GS$			22		nC		
Gate-Drain Charge	$Q_GD$			48		nC		
Turn-On Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}$ =100V, $V_{GS}$ =10V, $I_{D}$ =13A $R_{G}$ =25 $\Omega$ (Note 1, 2)		48		ns		
Turn-On Rise Time	$t_R$			59		ns		
Turn-Off Delay Time	$t_{D(OFF)}$			312		ns		
Turn-Off Fall Time	$t_{F}$			99		ns		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Continuous Drain-Source Diode					13	Α		
Forward Current	I <sub>S</sub>				13	A		
Maximum Pulsed Drain-Source Diode	1				26	Α		
Forward Current	I <sub>SM</sub>				20	^		
Drain-Source Diode Forward Voltage (Note 1)	$V_{SD}$	I <sub>S</sub> =13A, V <sub>GS</sub> =0V			1.4	V		
Body Diode Reverse Recovery Time (Note 1)	t <sub>rr</sub>	-I <sub>S</sub> =13A, V <sub>GS</sub> =0V, dI <sub>F</sub> /dt=100A/μs		1080		nS		
Body Diode Reverse Recovery Charge	$Q_{rr}$			21.8		μC		

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle≤2%.

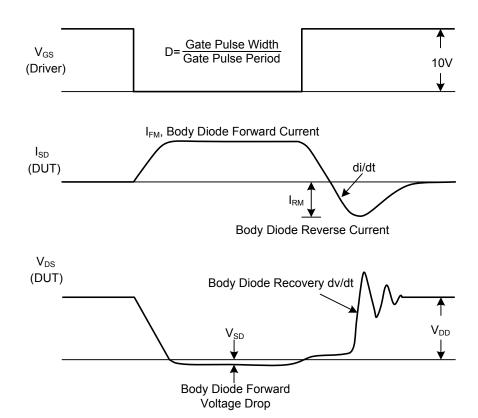
 $2. \ Essentially \ independent \ of \ operating \ temperature.$ 



# ■ TEST CIRCUITS AND WAVEFORMS

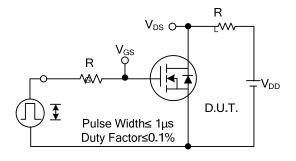


Peak Diode Recovery dv/dt Test Circuit

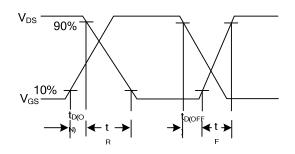


Peak Diode Recovery dv/dt Waveforms

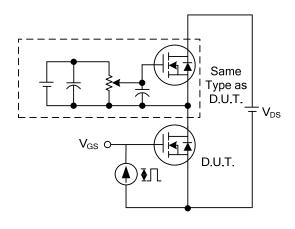
# ■ TEST CIRCUITS AND WAVEFORMS



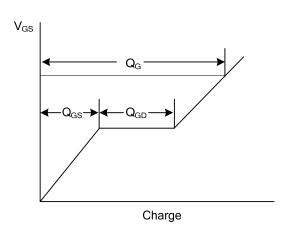
**Switching Test Circuit** 



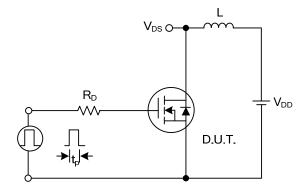
Switching Waveforms



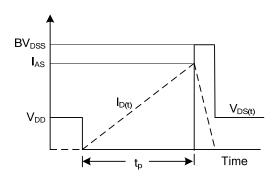
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

