## V10D100C-M3, V10D100CHM3

Vishay General Semiconductor

## **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low V<sub>F</sub> = 0.48 V at I<sub>F</sub> = 2.5 A



## V10D100C



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 5.0 A			
V <sub>RRM</sub>	100 V			
I <sub>FSM</sub>	100 A			
$V_F$ at $I_F$ = 5.0 A ( $T_A$ = 125 °C)	0.60 V			
T <sub>J</sub> max.	150 °C			
Package	TO-263AC (SMPD)			
Diode variations	Dual common cathode			

#### FEATURES

- Trench MOS Schottky technology generation 2
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
  Automotive ordering code: base P/NHM3
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, inductrial, and automotive application.

#### **MECHANICAL DATA**

Case: TO-263AC (SMPD)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test **Polarity:** As marked

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V10D100C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	100	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	10	А	
	per diode		5	A	
Maximum DC reverse voltage		V <sub>DC</sub>	160	V	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	100	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

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ROHS COMPLIANT

HALOGEN

FREE

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \degree C$ unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS SYMBOL		TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 2.5 A	$T_{A} = 25 ^{\circ}\text{C}$	V <sub>F</sub> <sup>(1)</sup>	0.55	-	V	
	I <sub>F</sub> = 5.0 A			0.67	0.75		
	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 125 °C		0.48	-		
	I <sub>F</sub> = 5.0 A			0.60	0.68		
Reverse current at rated V <sub>R</sub> per diode	V <sub>B</sub> = 70 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	2.3	-	μA	
	v <sub>R</sub> = 70 v	T <sub>A</sub> = 125 °C		2.3	-	mA	
	V <sub>B</sub> = 100 V	T <sub>A</sub> = 25 °C		-	500	μA	
	v <sub>R</sub> = 100 v	T <sub>A</sub> = 125 °C		7	20	mA	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V10D100C	UNIT	
Typical thermal resistance	per diode	$R_{ ext{ heta}JC}$	3.5		
	per device		2.5	°C/W	
	per device	R <sub>0JA</sub> <sup>(1)(2)</sup>	48		

#### Notes

 $^{(1)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{0JA}$  - junction-to-mount

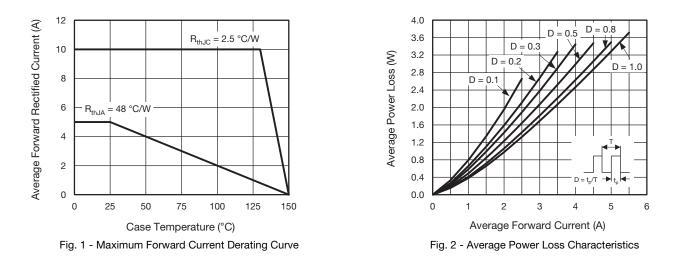
<sup>(2)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AC (SMPD)	V10D100C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel
TO-263AC (SMPD)	V10D100CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel

Note

(1) AEC-Q101 qualified

#### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)



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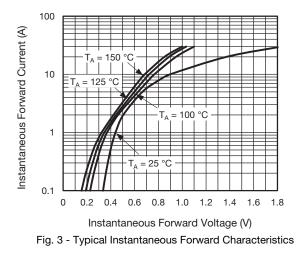
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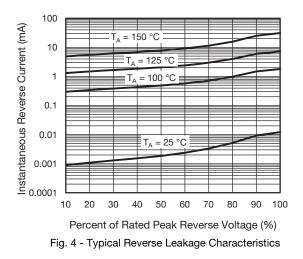
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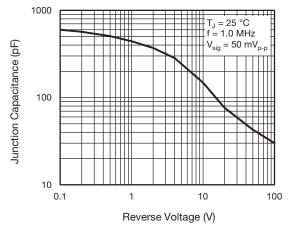
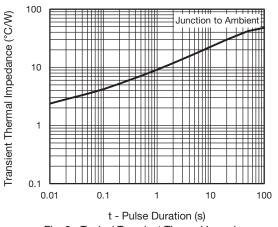
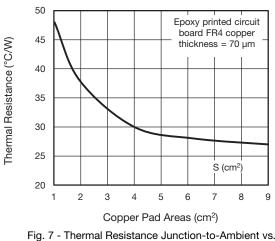


Fig. 5 - Typical Junction Capacitance





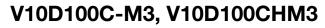


Copper Pad Areas

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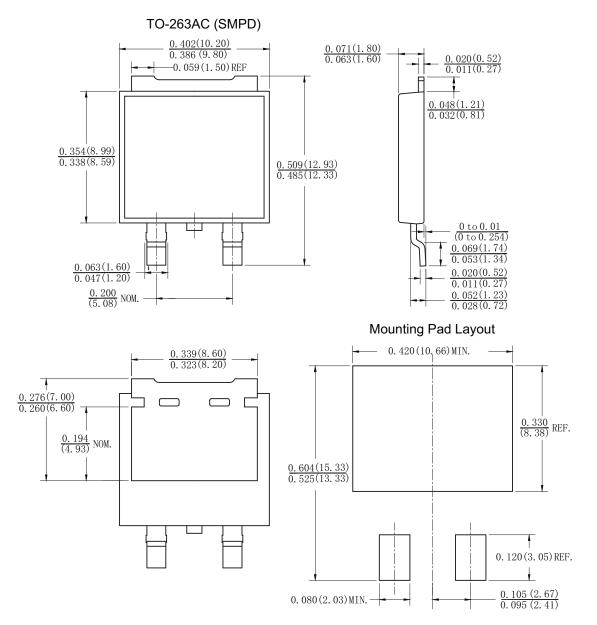


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#### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

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