



PRELIMINARY

SOLID STATE DEVICES, INC

14849 Firestone Boulevard · La Mirada, CA 90638
 Phone: (714) 670-SSDI (7734) · Fax: (714) 522-7424

Designer's Data Sheet

FEATURES:

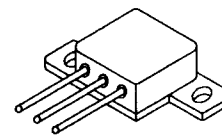
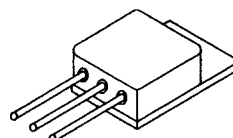
- Rugged construction with poly silicon gate
- Low RDS(on) and high transconductance
- Excellent high temperature stability
- Very fast switching speed
- Fast recovery and superior dv/dt performance
- Increased reverse energy capability
- Low input and transfer capacitance for easy paralleling
- Hermetically sealed
- TX, TXV and Space Level Screening available
- Replaces: IRF9130 Types

SFF9230M
SFF9230Z

-6.5 AMP
-200 VOLTS
0.80Ω
P-CHANNEL
POWER MOSFET

TO-254

TO-254Z



MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	VALUE	UNIT
Drain to Source Voltage	V _{DS}	-200	Volts
Gate to Source Voltage	V _{GS}	±20	Volts
Continuous Drain Current	I _D	-6.5	Amps
Operating and Storage Temperature	Top & Tstg	-55 to +150	°C
Thermal Resistance, Junction to Case	RθJC	2.0	°C/W
Total Device Dissipation @ TC=25°C	P _D	63	Watts
Total Device Dissipation @ TC=55°C		48	

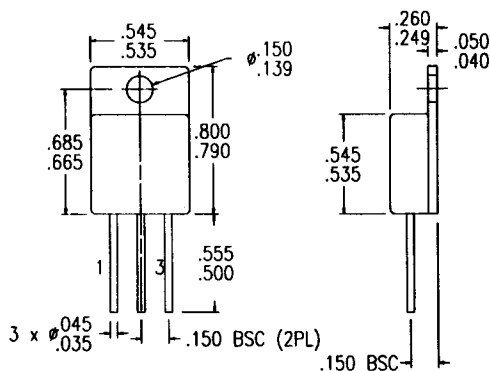
PACKAGE OUTLINE: TO-254

PIN OUT:

PIN 1: DRAIN

PIN 2: SOURCE

PIN 3: GATE



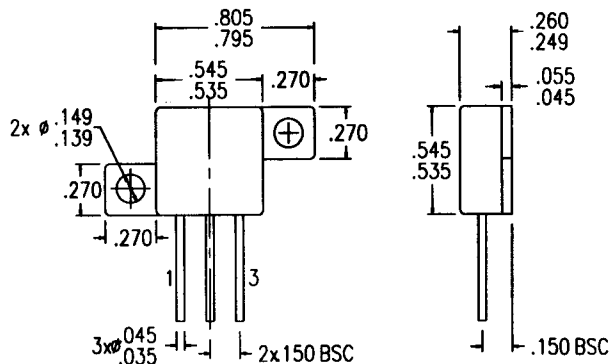
PACKAGE OUTLINE: TO-254Z

PIN OUT:

PIN 1: DRAIN

PIN 2: SOURCE

PIN 3: GATE



Available with Glass or Ceramic Seals. Contact Factory for details.

NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: FP0029 B

MED

SFF9230M

SFF9230Z

PRELIMINARY



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ELECTRICAL CHARACTERISTICS @ T_J=25 °C (Unless Otherwise Specified)

RATING		SYMBOL	MIN	TYP	MAX	UNIT
Drain to Source Breakdown Voltage (V _{GS} =0 V, I _D = -250μA)		BV _{DSS}	-200	---	---	V
Drain to Source on State Resistance (V _{GS} = -10 V, I _D =60% Rated ID)		R _{DS(on)}	---	0.5	0.8	Ω
On State Drain Current (V _{DS} > I _{D(on)} X R _{DS(on)} Max, V _{GS} = -10 V)		I _{D(on)}	-6.5	---	---	A
Gate Threshold Voltage (V _{DS} =V _{GS} , I _D = -250μA)		V _{GS(th)}	-2.0	---	-4.0	V
Forward Transconductance (V _{DS} > I _{D(on)} X R _{DS(on)} Max, I _{DS} =60% rated ID)		g _{fs}	2.2	3.5	---	S(Ω)
Zero Gate Voltage Drain Current (V _{DS} =max rated voltage, V _{GS} =0 V) (V _{DS} =80% rated V _{DS} , V _{GS} =0 V, T _A =125° C)		I _{DSS}	---	---	-250 -1000	μA
Gate to Source Leakage Forward Gate to Source Leakage Reverse	At rated V _{GS}	I _{GSS}	---	---	-100 100	nA
Total Gate Charge Gate to Source Charge Gate to Drain Charge	V _{GS} = -15 Volts 80% rated V _{DS} I _D = 8 A	Q _g Q _{gs} Q _{gd}	---	31 18 13	45 ---	nC
Turn on Delay Time Rise Time Turn Off Delay Time Fall Time	V _{DD} =50% rated V _{DS} 50% rated I _D R _G = 50 Ω	t _{d(on)} t _r t _{d(off)} t _f	---	30 50 50 40	50 100 100 80	nsec
Diode Forward Voltage (I _S =rated I _D , V _{GS} =0 V, T _J =25° C)		V _{SD}	---	---	-6.5	V
Diode Reverse Recovery Time Reverse Recovery Charge	T _J =150° C I _F =rated I _D di/dt=100 A/μsec	t _{rr} Q _{RR}	---	400 2.6	---	nsec μC
Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{GS} =0 Volts V _{DS} = -25 Volts f= 1 MHz	C _{iss} C _{oss} C _{rss}	---	550 170 50	650 300 90	pF

For thermal derating curves and other characteristic curves please contact SSDI Marketing Department.