

BLC6G20-110; BLC6G20LS-110

UHF power LDMOS transistor

Rev. 01 — 30 January 2006

Objective data sheet

1. Product profile

1.1 General description

110 W LDMOS power transistor for base station applications at frequencies from 1800 MHz to 2000 MHz.

Table 1: Typical performance

RF performance at $T_{case} = 25^{\circ}\text{C}$ in a common source class-AB production test circuit.

Mode of operation	f (MHz)	V _{DS} (V)	P _L (W)	G _p (dB)	η_D (%)	ACPR ₄₀₀ (dBc)	ACPR ₆₀₀ (dBc)	EVM (%)	IMD3 (dBc)	ACPR (dBc)
CW	1930 to 1990	28	100	17	51	-	-	-	-	-
GSM EDGE	1930 to 1990	28	48 (AV)	17.5	40	-60	-70	2.1	-	-
2-carrier W-CDMA	1930 to 1990	28	25 (AV)	18	32	-	-	-	-37 [1]	-40 [1]

[1] Test signal: 3GPP; test model 1; 64 DPCH; PAR = 7 dB at 0.01 % probability on CCDF per carrier; carrier spacing 10 MHz.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Typical 2-carrier W-CDMA performance at frequencies of 1930 MHz and 1990 MHz, a supply voltage of 28 V and an I_{DQ} of 950 mA:
 - ◆ Output power = 25 W (AV)
 - ◆ Gain = 18 dB
 - ◆ Efficiency = 32 %
 - ◆ IMD3 = -37 dBc
 - ◆ ACPR = -40 dBc
- Easy power control
- Integrated ESD protection
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (1800 MHz to 2000 MHz)
- Internally matched for ease of use

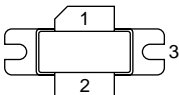
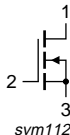
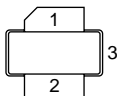
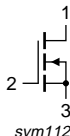
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1.3 Applications

- RF power amplifiers for GSM, GSM EDGE, W-CDMA and CDMA base stations and multi carrier applications in the 1800 MHz to 2000 MHz frequency range.

2. Pinning information

Table 2: Pinning

Pin	Description	Simplified outline	Symbol
BLC6G20-110 (SOT895-1)			
1	drain		
2	gate		
3	source		
BLC6G20LS-110 (SOT896-1)			
1	drain		
2	gate		
3	source		

[1] Connected to flange

3. Ordering information

Table 3: Ordering information

Type number	Package		
	Name	Description	Version
BLC6G20-110	-	plastic flanged cavity package; 2 mounting slots; 2 leads	SOT895-1
BLC6G20LS-110	-	plastic earless flanged cavity package; 2 leads	SOT896-1

4. Limiting values

Table 4: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage		-	65	V
V_{GS}	gate-source voltage		-0.5	+13	V
I_D	drain current		-	<tb>	A
T_{stg}	storage temperature		-65	+150	°C
T_j	junction temperature		-	225	°C

5. Thermal characteristics

Table 5: Thermal characteristics

Symbol	Parameter	Conditions	Type	Min	Typ	Max	Unit
$R_{th(j-case)}$	thermal resistance from junction to case	$T_{case} = 80\text{ }^{\circ}\text{C}$; $P_L = 25\text{ W}$	BLC6G20-110	<tbid>	<tbid>	<tbid>	K/W
			BLC6G20LS-110	<tbid>	0.45	0.54	K/W

6. Characteristics

Table 6: Characteristics

$T_j = 25\text{ }^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0\text{ V}$; $I_D = 0.5\text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 10\text{ V}$; $I_D = 150\text{ mA}$	<tbid>	2	<tbid>	V
V_{GSq}	gate-source quiescent voltage	$V_{DS} = 28\text{ V}$; $I_D = 950\text{ mA}$	<tbid>	<tbid>	<tbid>	V
I_{DSS}	drain leakage current	$V_{GS} = 0\text{ V}$; $V_{DS} = 28\text{ V}$	-	-	5	μA
I_{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75\text{ V}$; $V_{DS} = 10\text{ V}$	23	27	-	A
I_{GSS}	gate leakage current	$V_{GS} = 13\text{ V}$; $V_{DS} = 0\text{ V}$	-	-	450	nA
g_{fs}	forward transconductance	$V_{DS} = 10\text{ V}$; $I_D = 7.5\text{ A}$	-	10.5	-	S
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75\text{ V}$; $I_D = 5.25\text{ A}$	-	0.1	0.125	Ω
C_{rs}	feedback capacitance	$V_{GS} = 0\text{ V}$; $V_{DS} = 28\text{ V}$; $f = 1\text{ MHz}$	-	<tbid>	-	pF

7. Application information

Table 7: Application information

Mode of operation: 2-carrier W-CDMA; PAR 7 dB at 0.01 % probability on CCDF; 3GPP test model 1; 1-64 PDPCH; $f_1 = 1932.5\text{ MHz}$; $f_2 = 1942.5\text{ MHz}$; $f_3 = 1977.5\text{ MHz}$; $f_4 = 1987.5\text{ MHz}$; RF performance at $V_{DS} = 28\text{ V}$; $I_{Dq} = 950\text{ mA}$; $T_{case} = 25\text{ }^{\circ}\text{C}$; unless otherwise specified; in a class-AB production test circuit

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$P_{L(AV)}$	average output power		-	25	-	W
G_p	power gain	$P_{L(AV)} = 25\text{ W}$	-	18	-	dB
η_D	drain efficiency	$P_{L(AV)} = 25\text{ W}$	-	32	-	%
IMD3	third order intermodulation distortion	$P_{L(AV)} = 25\text{ W}$	-	-37	-	dBc
ACPR	adjacent channel power ratio	$P_{L(AV)} = 25\text{ W}$	-	-40	-	dBc

7.1 Ruggedness in class-AB operation

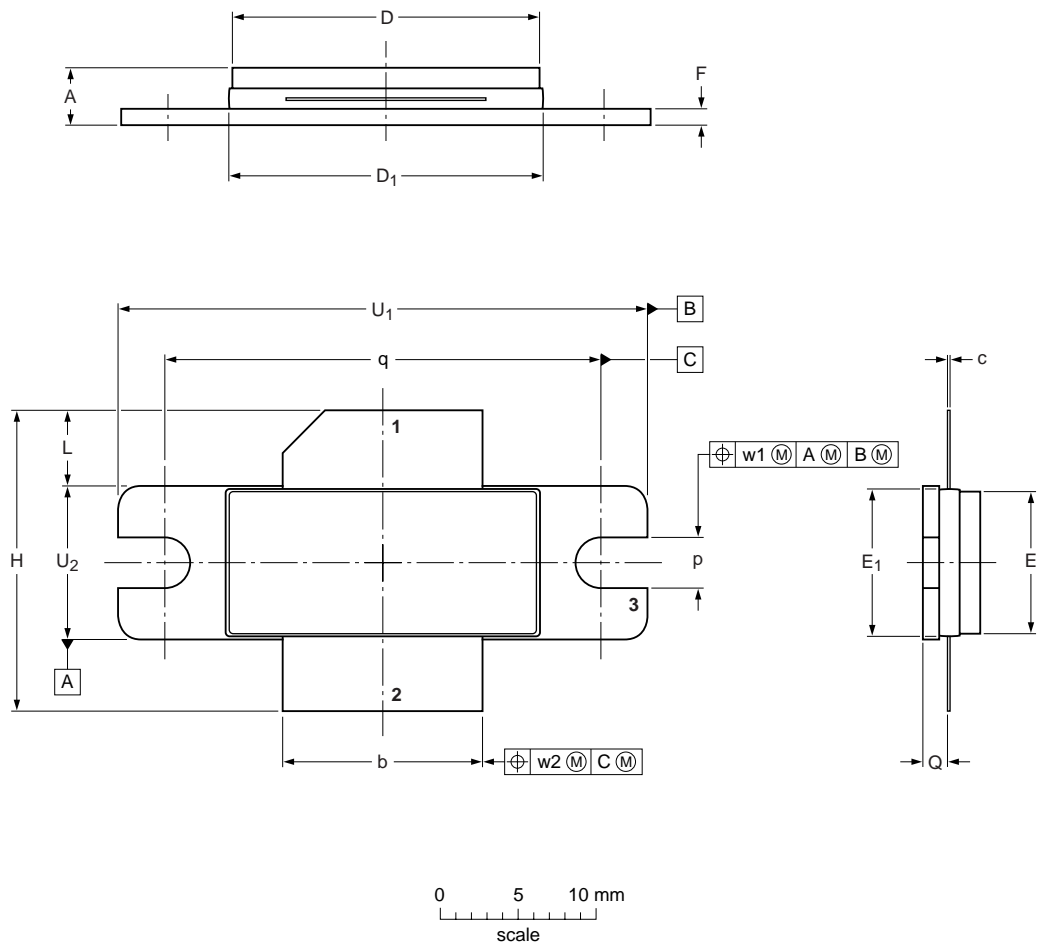
The BLC6G20-110 and BLC6G20LS-110 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:

$V_{DS} = 28\text{ V}$; $I_{Dq} = 950\text{ mA}$; $P_L = 110\text{ W (CW)}$; $f = 1990\text{ MHz}$.

8. Package outline

Plastic flanged cavity package; 2 mounting slots; 2 leads

SOT895-1



DIMENSIONS (mm are the original dimensions)

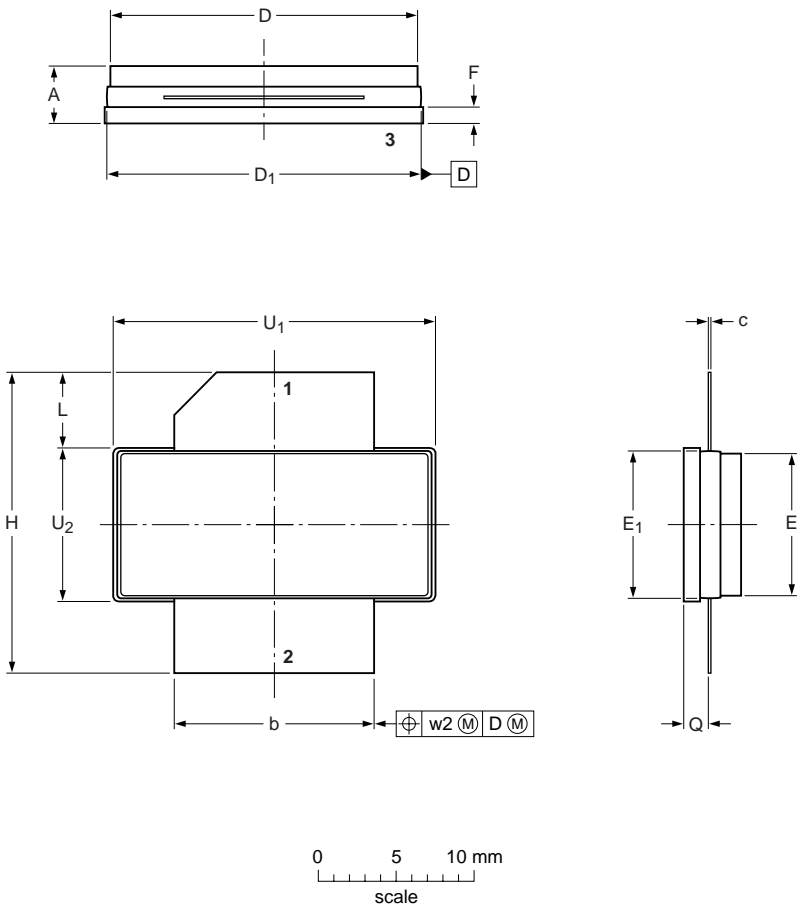
UNIT	A	b	c	D	D ₁	E	E ₁	F	H	L	p	Q	q	U ₁	U ₂	w ₁	w ₂
mm	4.1 3.3	12.83 12.57	0.17 0.14	19.9 19.7	20.42 20.12	9.53 9.27	9.78 9.53	1.14 0.89	19.94 18.92	5.3 4.5	3.38 3.12	1.75 1.50	27.94	34.16 33.91	9.91 9.65	0.25	0.6
inches	0.161 0.130	0.505 0.495	0.0065 0.0055	0.785 0.775	0.804 0.792	0.375 0.365	0.385 0.375	0.045 0.035	0.785 0.745	0.209 0.177	0.133 0.123	0.069 0.059	1.100	1.345 1.335	0.390 0.380	0.01	0.023

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT895-1						05-06-22 05-06-28

Fig 1. Package outline SOT895-1

Plastic earless flanged cavity package; 2 leads

SOT896-1



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	c	D	D ₁	E	E ₁	F	H	L	Q	U ₁	U ₂	w ₂
mm	4.1 3.3	12.83 12.57	0.17 0.14	19.9 19.7	20.42 20.12	9.53 9.27	9.78 9.53	1.14 0.89	19.94 18.92	5.3 4.5	1.75 1.50	20.70 20.45	9.91 9.65	0.6
inches	0.161 0.130	0.505 0.495	0.0065 0.0055	0.785 0.775	0.804 0.792	0.375 0.365	0.385 0.375	0.045 0.035	0.785 0.745	0.209 0.177	0.069 0.059	0.815 0.805	0.390 0.380	0.023

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT896-1						05-06-22 05-06-28

Fig 2. Package outline SOT896-1

9. Abbreviations

Table 8: Abbreviations

Acronym	Description
3GPP	Third Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CDMA	Code Division Multiple Access
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
EDGE	Enhanced Data rates for GSM Evolution
EVM	Error Vector Magnitude
GSM	Global System for Mobile communications
LDMOS	Laterally Diffused Metal Oxide Semiconductor
PAR	Peak-to-Average power Ratio
PDPCH	transmission Power of the Dedicated Physical CHannel
RF	Radio Frequency
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access



10. Revision history

Table 9: Revision history

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
BLC6G20-110_6G20 LS-110_1	20060130	Objective data sheet	-	-	-

11. Data sheet status

Level	Data sheet status ^[1]	Product status ^{[2] [3]}	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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