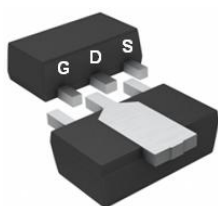


P06P03LCGA

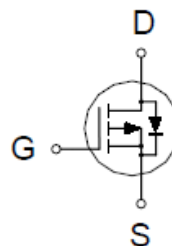
P-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
-30V	45mΩ @ $V_{GS} = -10V$	-4A



SOT-89



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current	$T_A = 25\text{ }^{\circ}\text{C}$	I_D	-4	A
	$T_A = 70\text{ }^{\circ}\text{C}$		-3	
Pulsed Drain Current ¹		I_{DM}	-20	
Avalanche Current		I_{AS}	-19	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	18	mJ
Power Dissipation	$T_A = 25\text{ }^{\circ}\text{C}$	P_D	1.34	W
	$T_A = 70\text{ }^{\circ}\text{C}$		0.86	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		18	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		93	

¹ Pulse width limited by maximum junction temperature.

P06P03LCGA

P-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1	-1.5	-3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24V, V_{GS} = 0V$			-1	μA
		$V_{DS} = -20V, V_{GS} = 0V, T_J = 125\text{ }^{\circ}C$			-10	
On-State Drain Current ¹	$I_{D(ON)}$	$V_{DS} = -10V, V_{GS} = -10V$	-20			A
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = -4.5V, I_D = -3A$		62	75	mΩ
		$V_{GS} = -10V, I_D = -3.5A$		39	45	
Forward Transconductance ¹	g_{fs}	$V_{DS} = -10V, I_D = -3.5A$		11		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = -15V, f = 1MHz$		557		pF
Output Capacitance	C_{oss}			101		
Reverse Transfer Capacitance	C_{rss}			90		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		10		Ω
Total Gate Charge ²	Q_g	$V_{DS} = -15V, V_{GS} = -10V, I_D = -3.5A$		13		nC
Gate-Source Charge ²	Q_{gs}			1.6		
Gate-Drain Charge ²	Q_{gd}			3.9		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DS} = -15V, I_D \cong -3.5A, V_{GS} = -10V, R_{GS} = 6\Omega$		5.7		nS
Rise Time ²	t_r			10		
Turn-Off Delay Time ²	$t_{d(off)}$			18		
Fall Time ²	t_f			5		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current	I _S				-4	A
Forward Voltage ¹	V _{SD}	I _F = -1A, V _{GS} = 0V			-1.2	V
Reverse Recovery Time	t _{rr}	I _F = -3.5A, dI _F /dt = 100A / μS		15.5		nS
Reverse Recovery Charge	Q _{rr}			7.9		nC

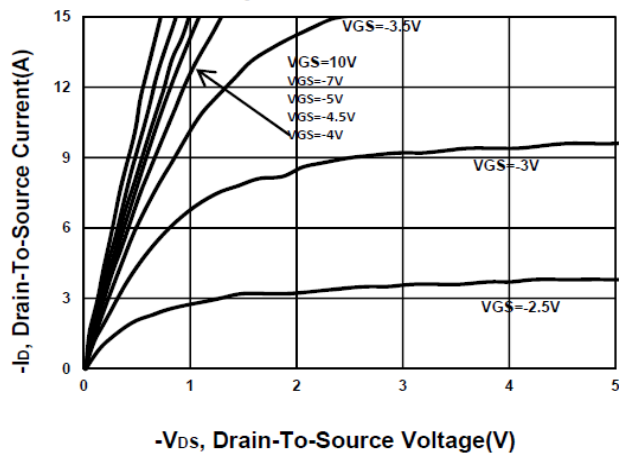
¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

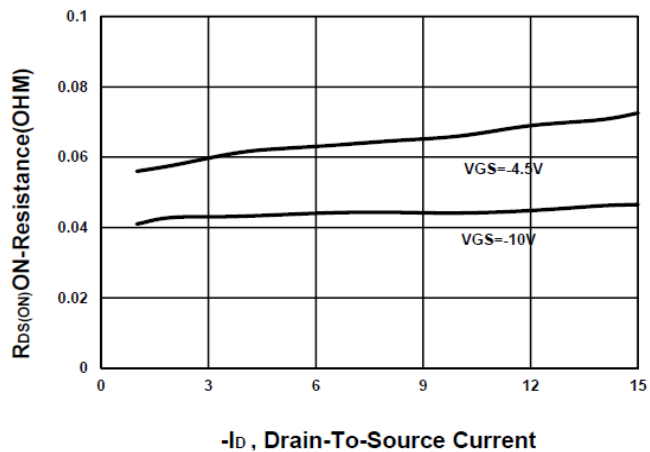
P06P03LCGA

P-Channel Enhancement Mode MOSFET

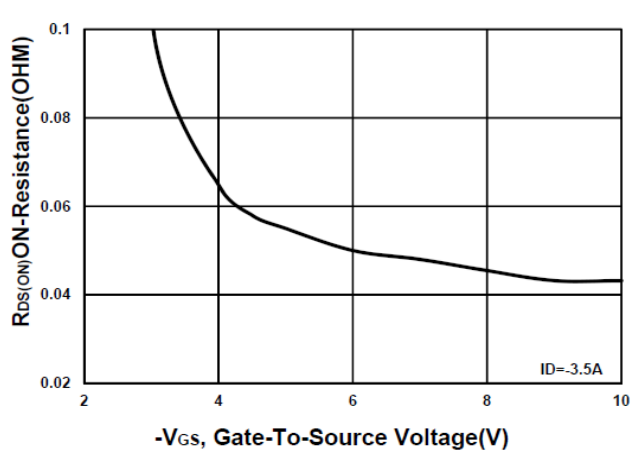
Output Characteristics



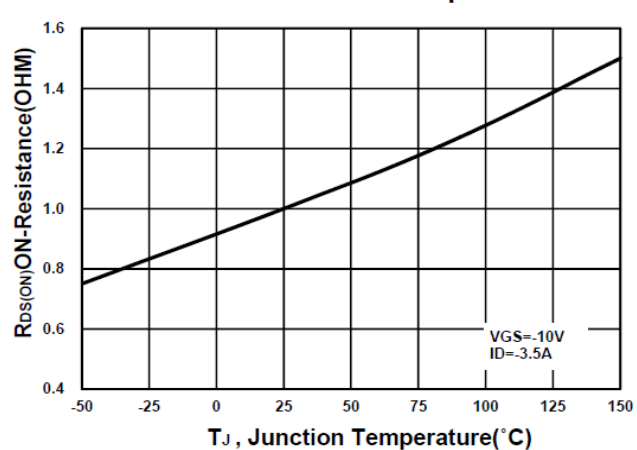
On-Resistance VS Drain Current



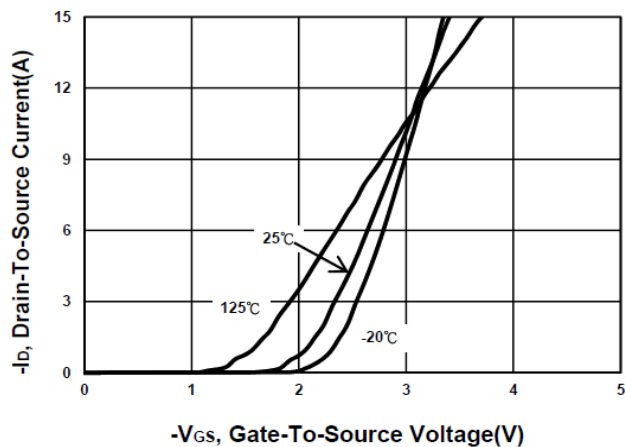
On-Resistance VS Gate-To-Source



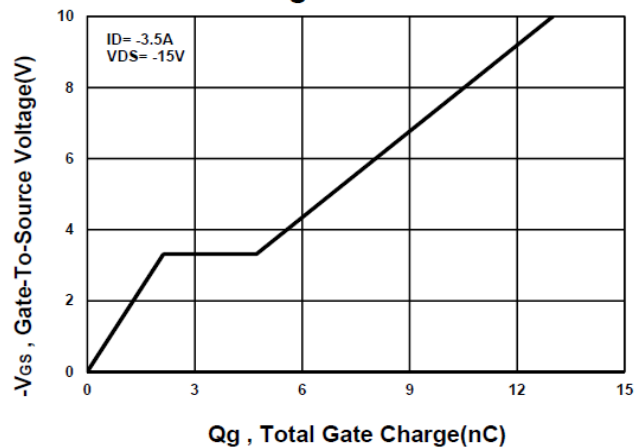
On-Resistance VS Temperature



Transfer Characteristics



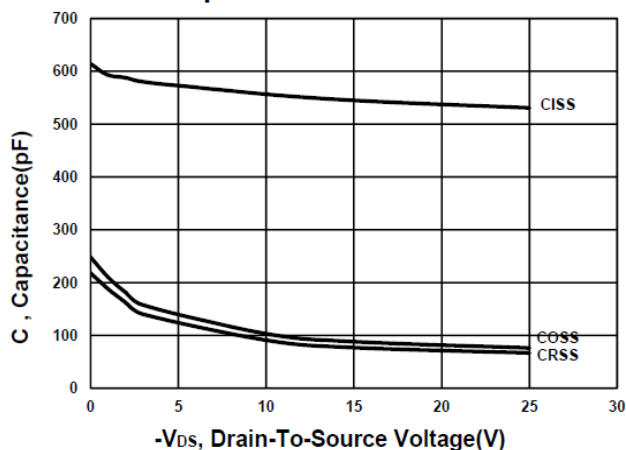
Gate charge Characteristics



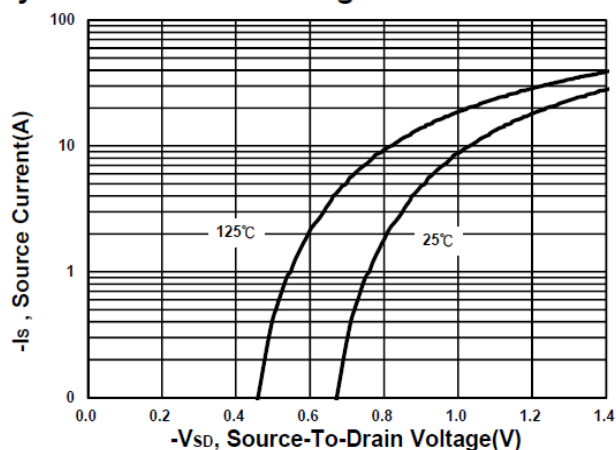
P06P03LCGA

P-Channel Enhancement Mode MOSFET

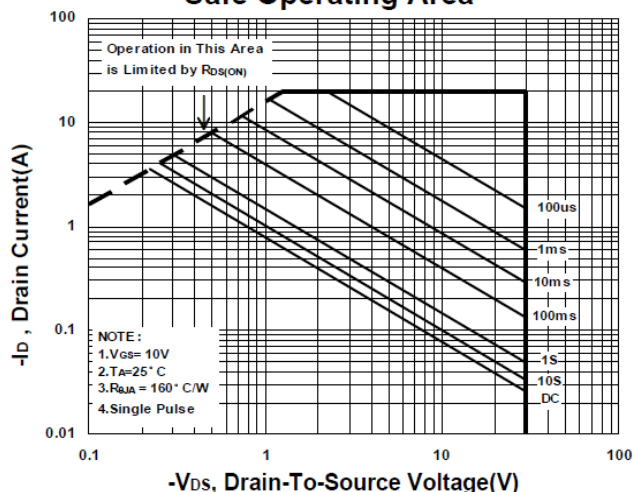
Capacitance Characteristic



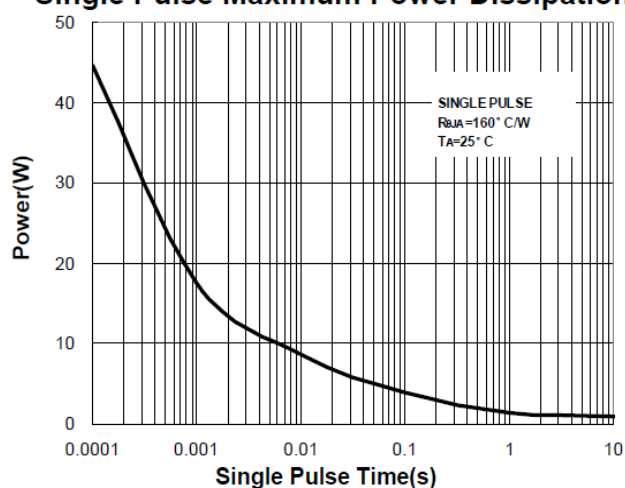
Body Diode Forward Voltage VS Source current



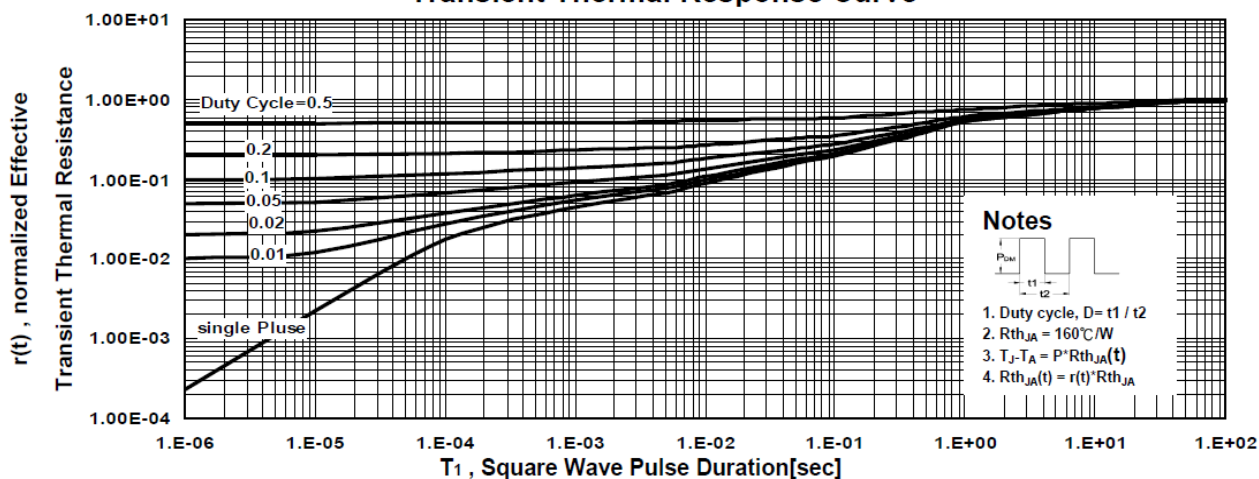
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



P06P03LCGA

P-Channel Enhancement Mode MOSFET

Package Dimension

SOT-89 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.45	4.5	4.55	G	0.36	0.50	0.56
B	1.4	1.7	1.8	H	1.3	1.5	1.7
C	0	0.7	1.05	I	2.8	3.0	3.2
D	2.3	2.5	2.6	J	1.4	1.5	1.6
E	0.8	1.04	1.2	K	3.8	4.2	4.25
F	0.3	0.46	0.52	L	0.35	0.4	0.44

